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# DAKAR10F/FG/R/RG

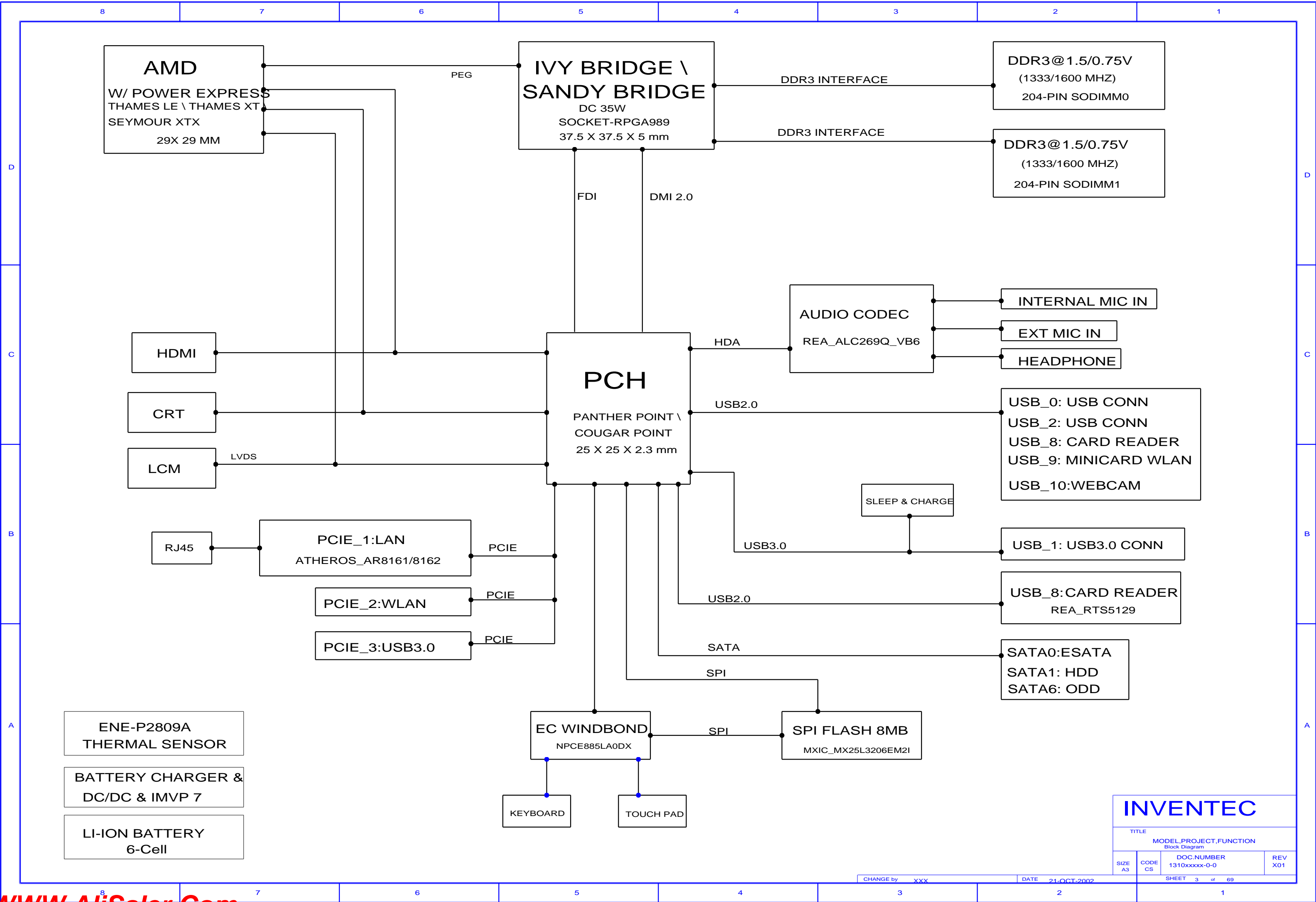
2011.10.28

21-OCT-2002		
DATE	CHANGE NO.	REV

DRAWER		EE	DATE	POWER	DATE	<div>INVENTEC</div> <div>TITLEMODEL,PROJECT,FUNCTION Everest Main Board</div>				
DESIGN										
CHECK										
RESPONSIBLE										
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FILE NAME:						C		CS	1310xxxxx-0-0	X01
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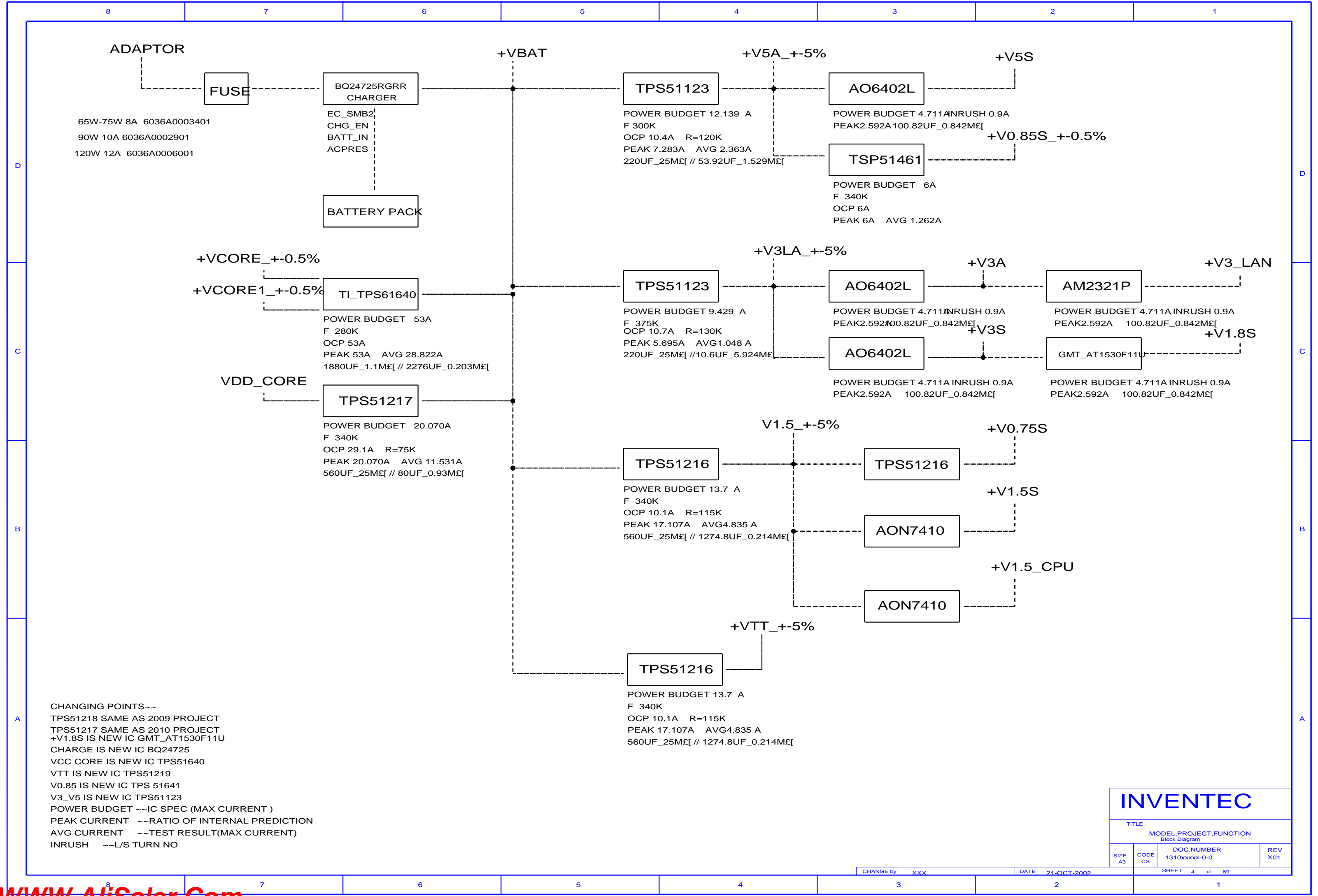
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2. INDEX	27. MINI1 WLAN/DEBUG CARD	52. PCH 6 MISC
3. BLOCK DIAGRAM	28. MINI2 3G/LTE	53. PCH 7 POWER
4. POWER MAP	29. SATA HDD/ODD CONN	54. PCH 8 POWER
5. POWER CHARGER	30. USB 2.0 CONN	55. PCH 9 GND
6. POWER +V3LA/+V3A/+5A	31. USB 3.0 CONTROLLER	56. VGA 1
7. POWER +V1.5/+V0.75	32. USB 3.0 CONN W/ S&C	57. VGA 2
8. POWER +V1.8S	33. USB 3.0 CONN	58. VGA 3
9. POWER VCCIO	34. LCM CONN	59. VGA 4
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11. POWER VCORE	36. HDMI CONN	61. VGA 6
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16. LOAD SWITCH-2	41. CPU 1	66. USB BOARD
17. PCB SCREW	42. CPU 2	67. PICK BUTTON BOARD
18. HALL SENSOR	43. CPU 3 DRAM	68. POWER BUTTON BOARD
19. LED	44. CPU 4 POWER	69. EMI
20. K/B & TP/B CONN	45. CPU 5 POWER	
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CHANGING POINTS~~  
TPS51218 SAME AS 2009 PROJECT  
TPS51217 SAME AS 2010 PROJECT  
+V1.8S IS NEW IC GMT\_AT1530F11U  
CHARGE IS NEW IC BQ24725  
VCC CORE IS NEW IC TPS51640  
VTT IS NEW IC TPS51219  
V0.85 IS NEW IC TPS 51641  
V3\_V5 IS NEW IC TPS51123  
POWER BUDGET ~~IC SPEC (MAX CURRENT )  
PEAK CURRENT ~~RATIO OF INTERNAL PREDICTION  
AVG CURRENT ~~TEST RESULT(MAX CURRENT)  
INRUSH ~~L/S TURN NO

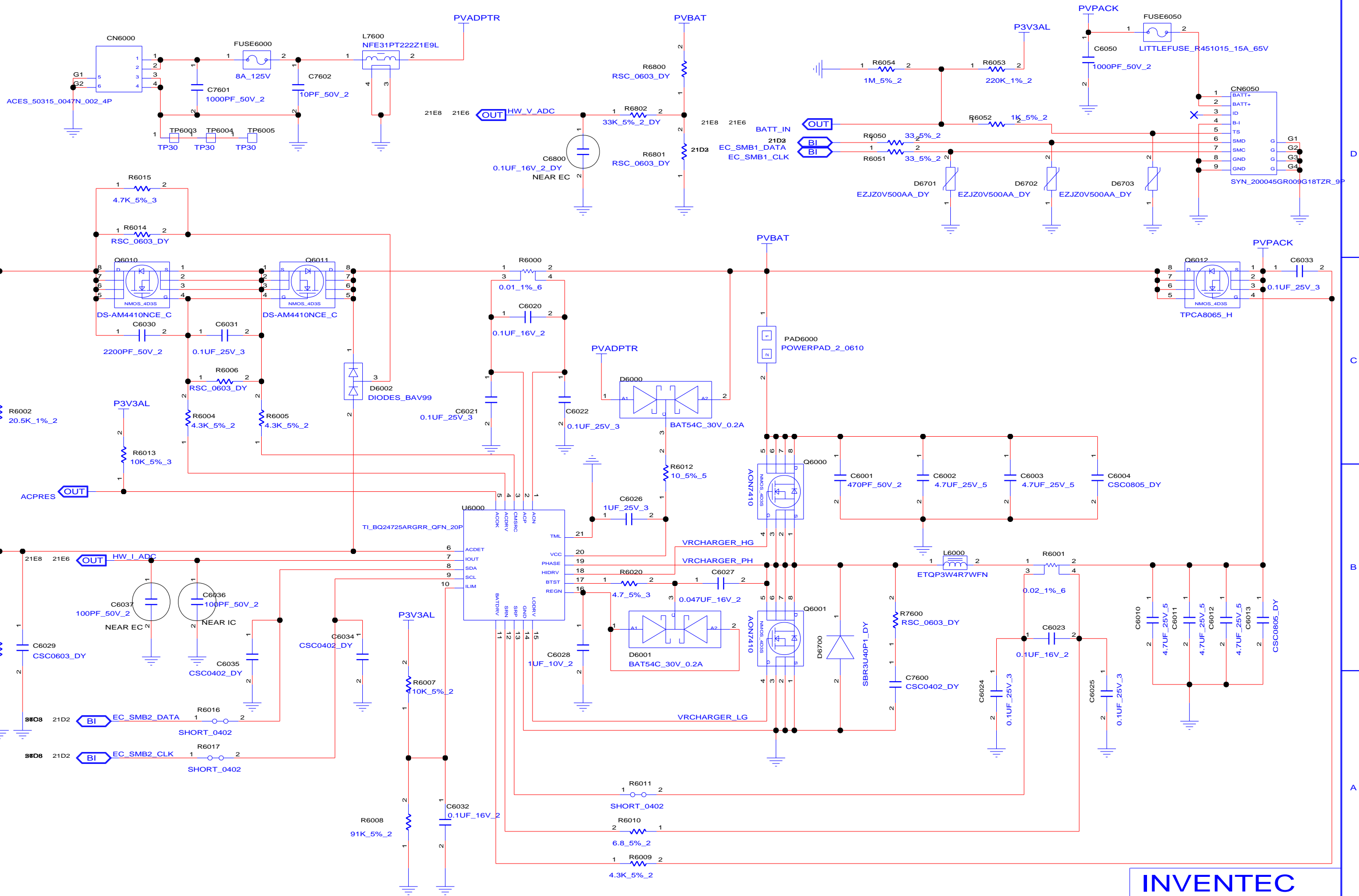
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FUSE6000  
65W-75W 8A(6036A0003401)  
90W 10A(6036A0002901)  
120W 12A(6036A0006001)

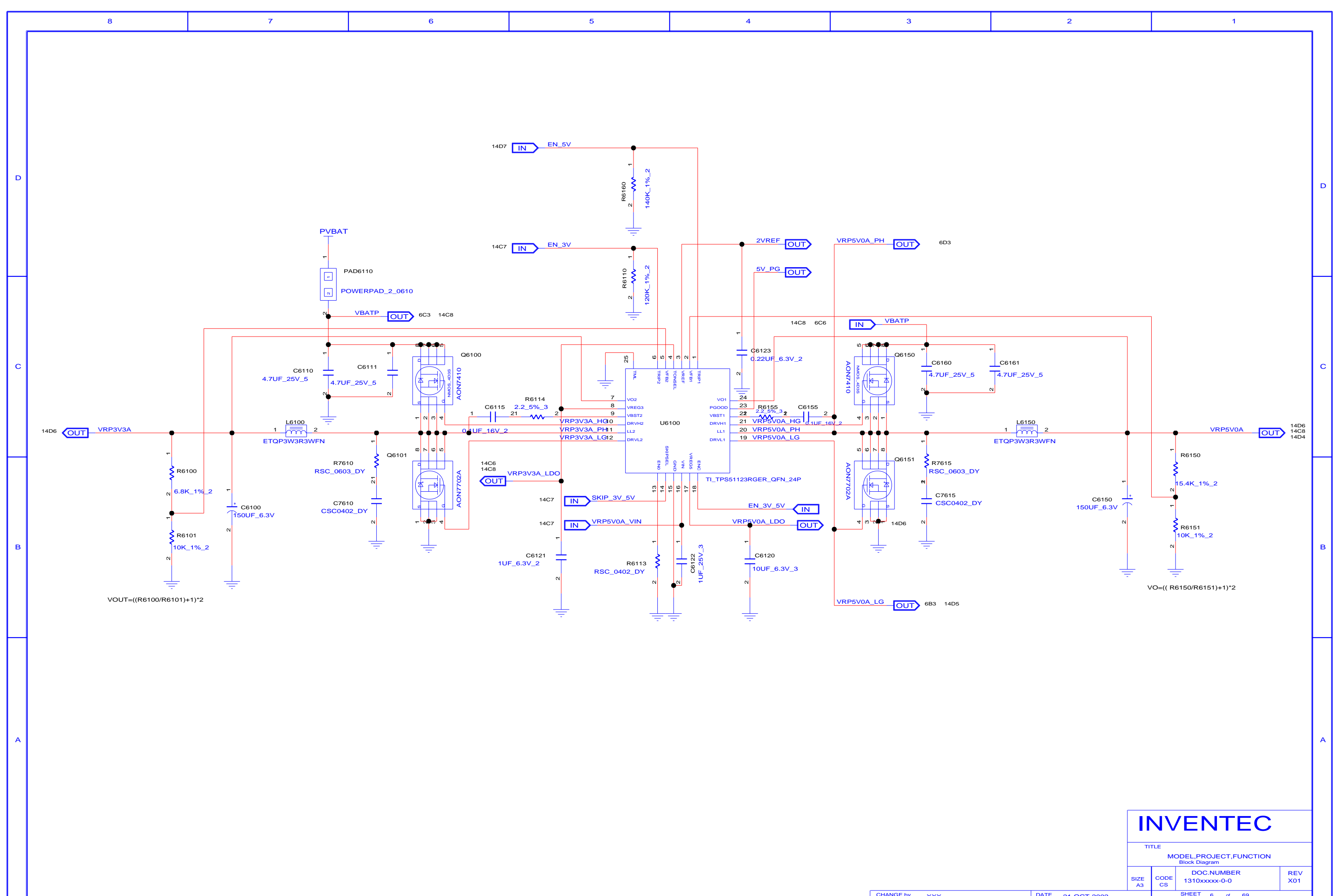


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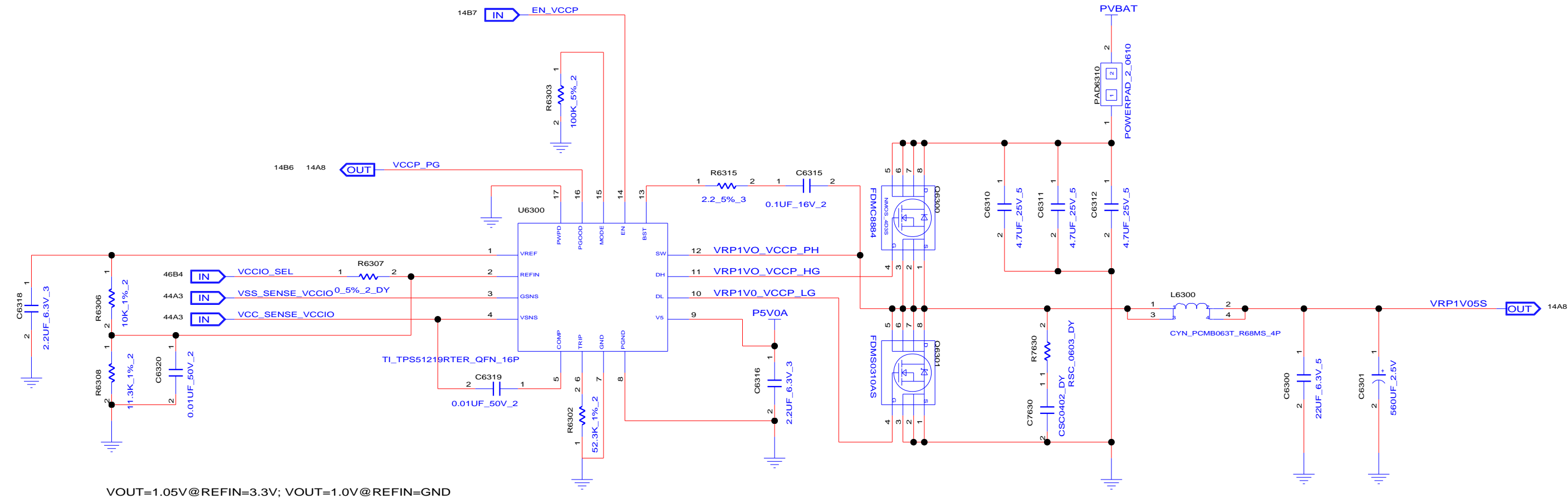
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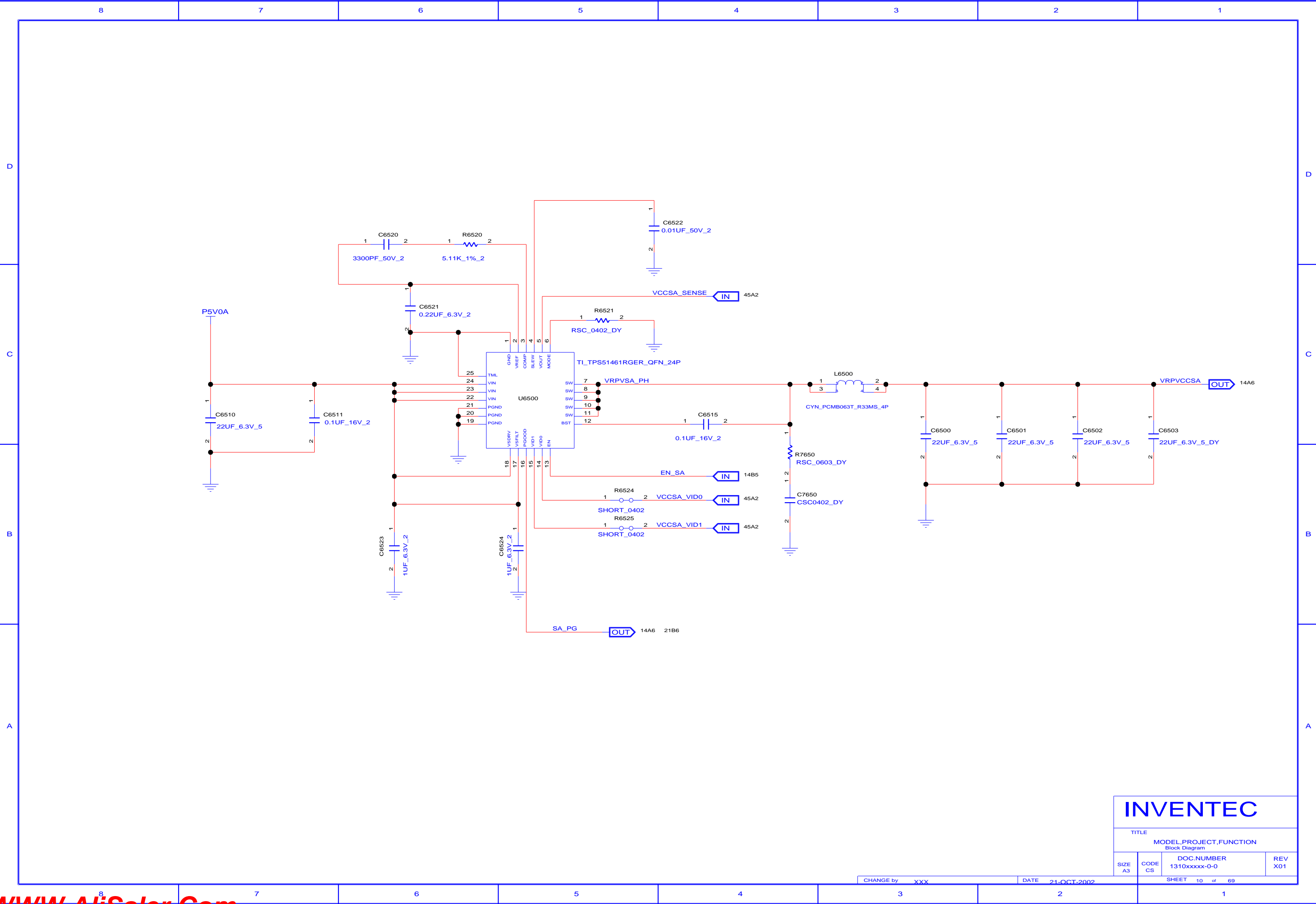






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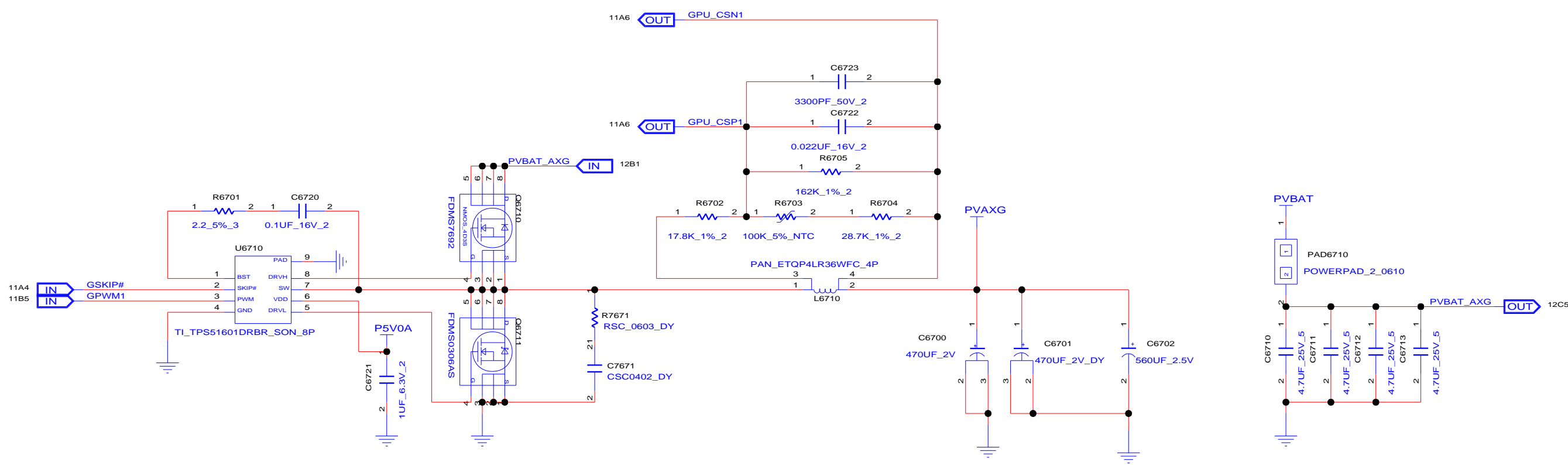
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A

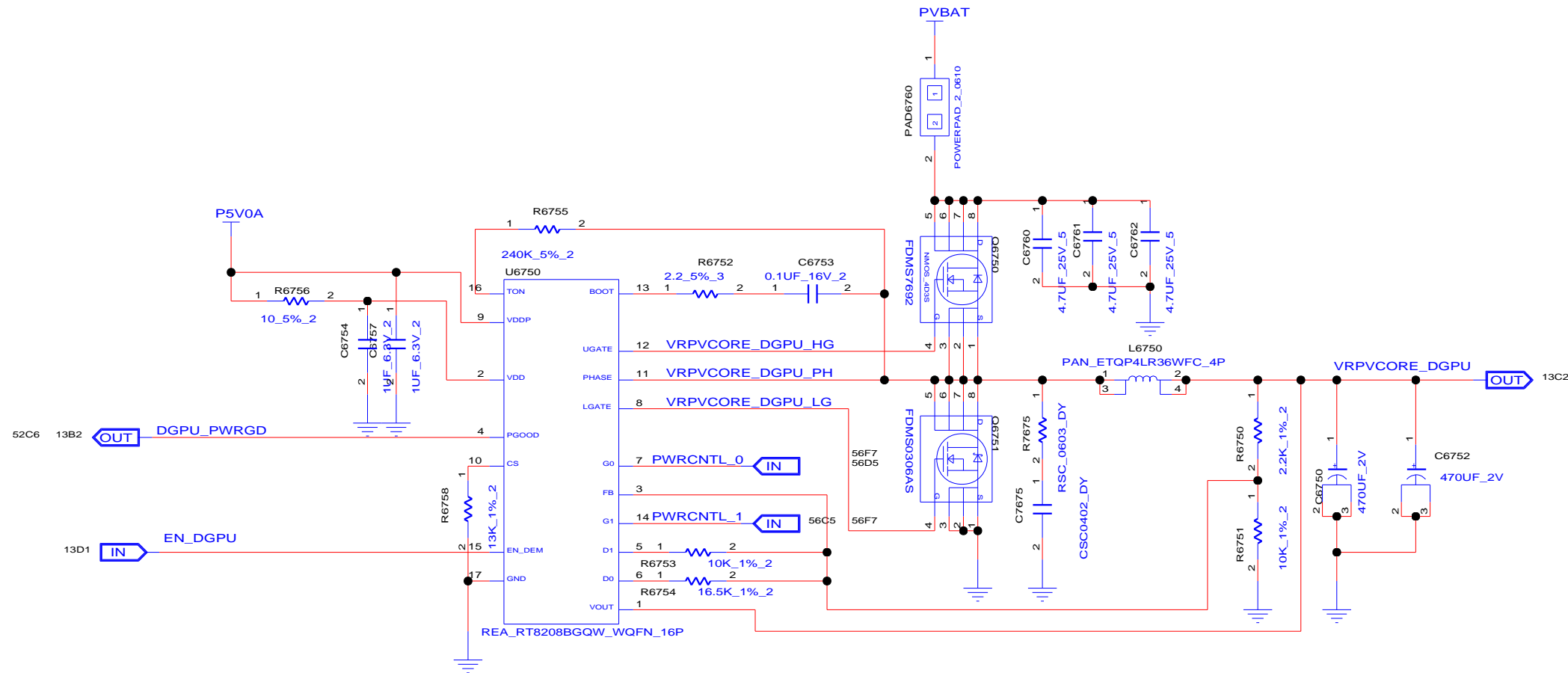


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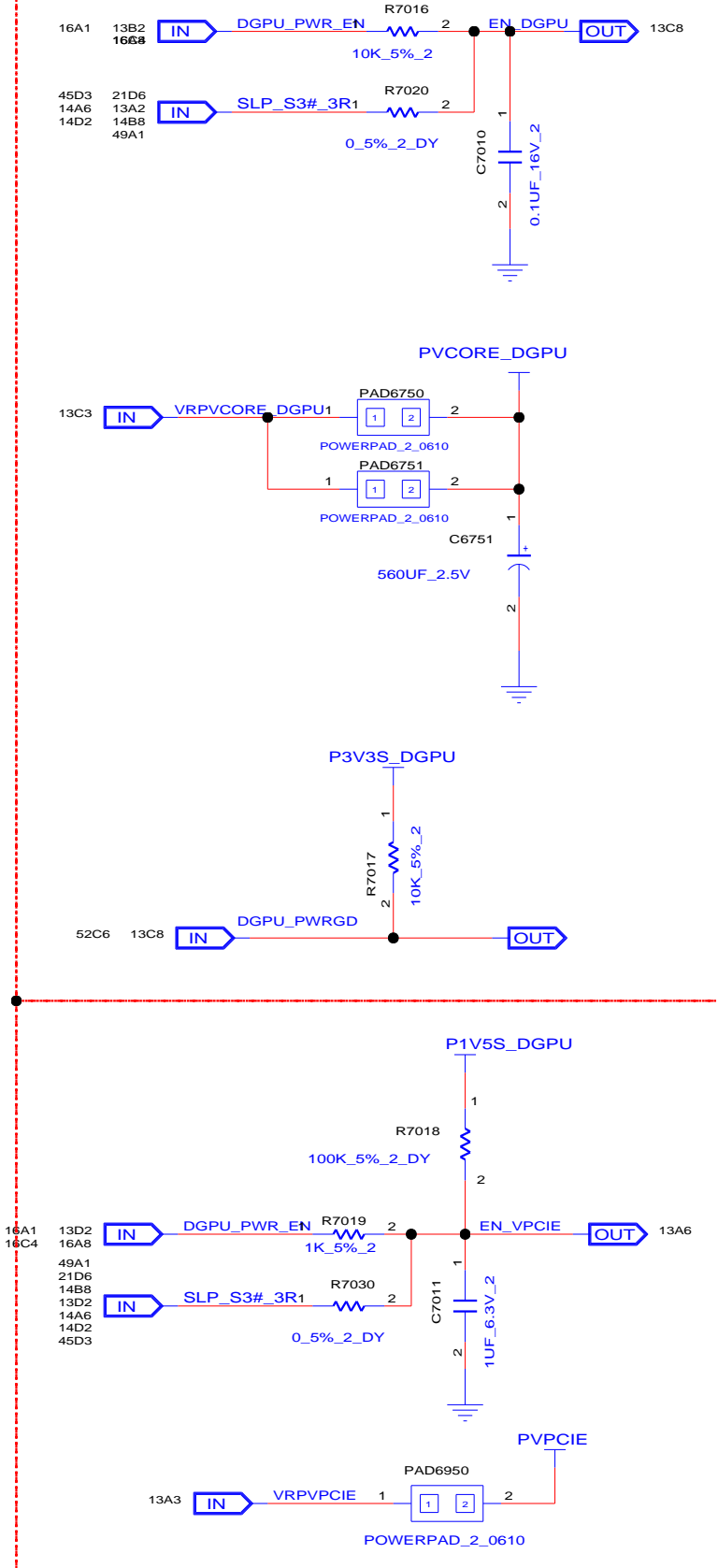
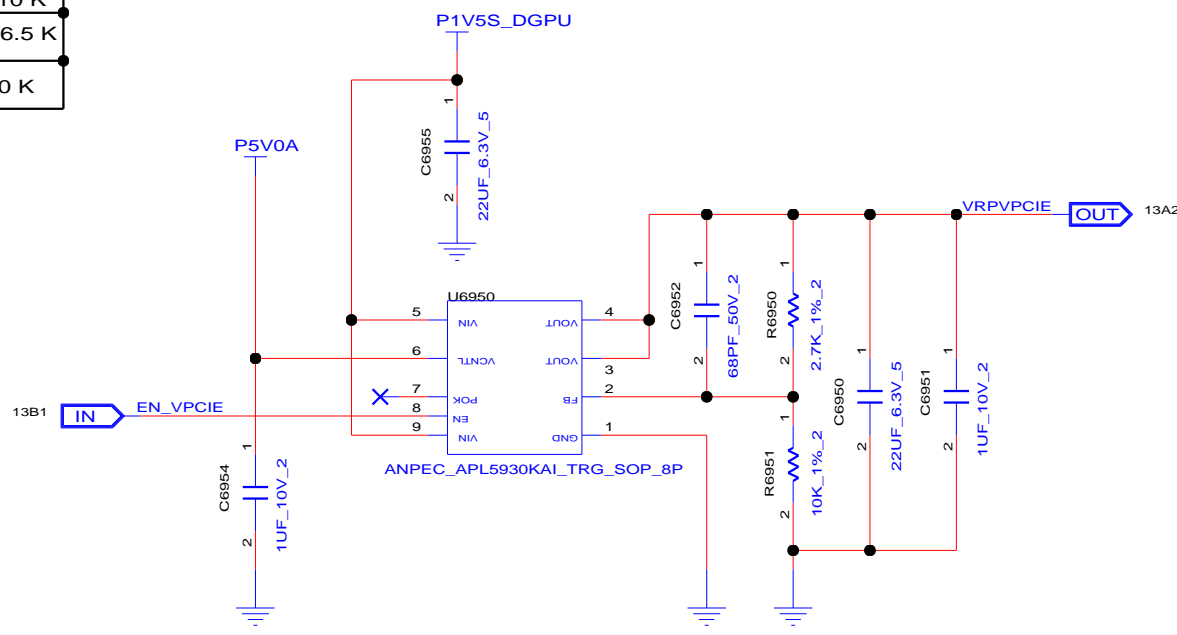
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VGA TYPE_I		SEYMOUR XTX	THAMES XT	THAMES LE		
G0	G1	VOUT	VOUT	VOUT	RESISTOR VALUE	
0	0	1.15	N/A	N/A	R6750(R1)	2.2 K
0	1	1.05	N/A	N/A	R6751(R2)	10 K
1	0	1	1	1	R6754(R3)	16.5 K
1	1	0.9	0.9	0.9	R6753(R4)	10 K

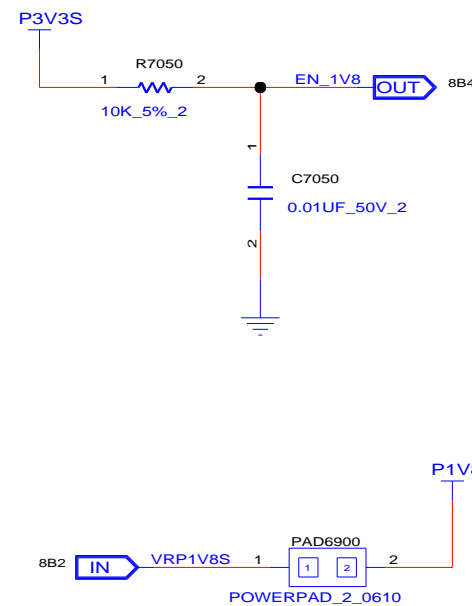
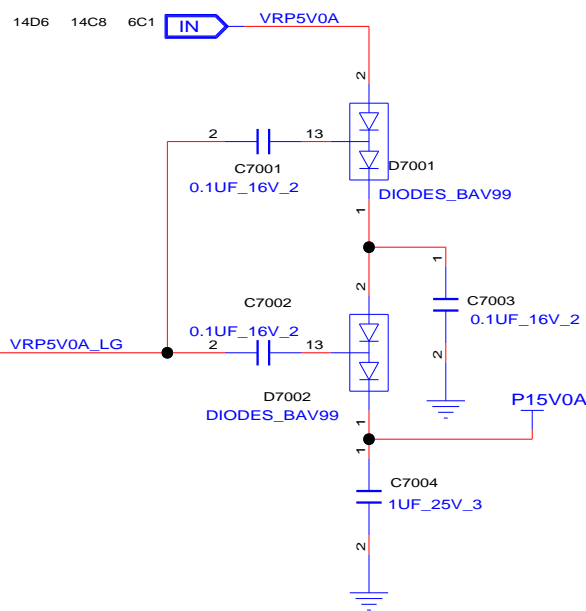
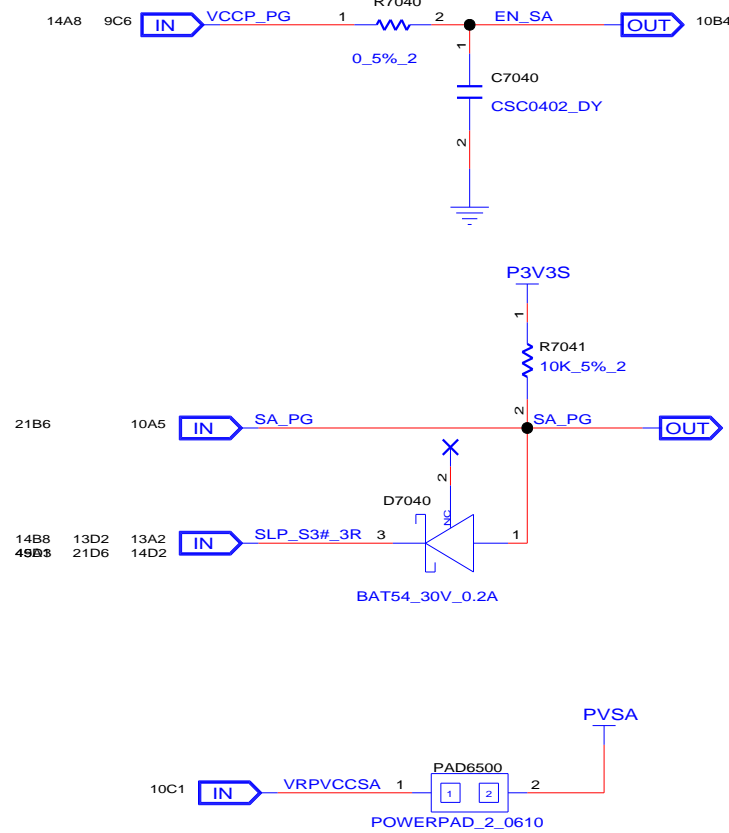
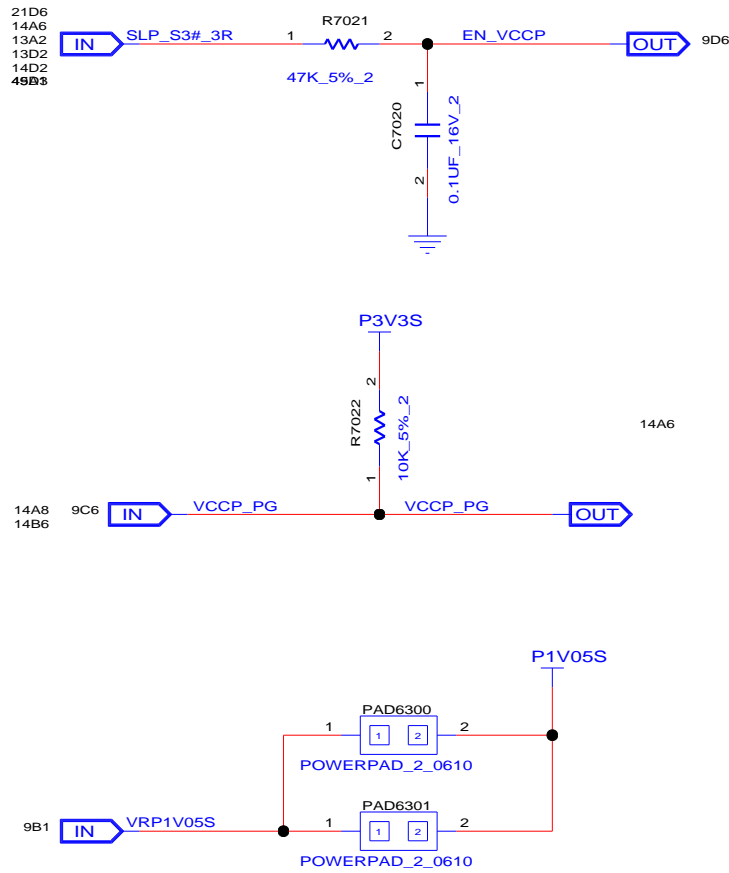
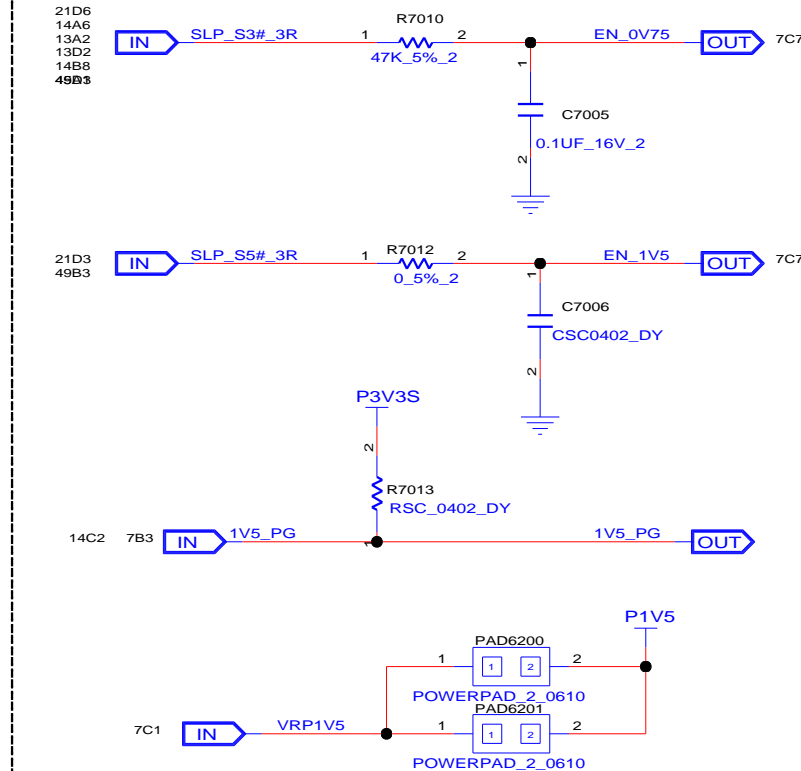
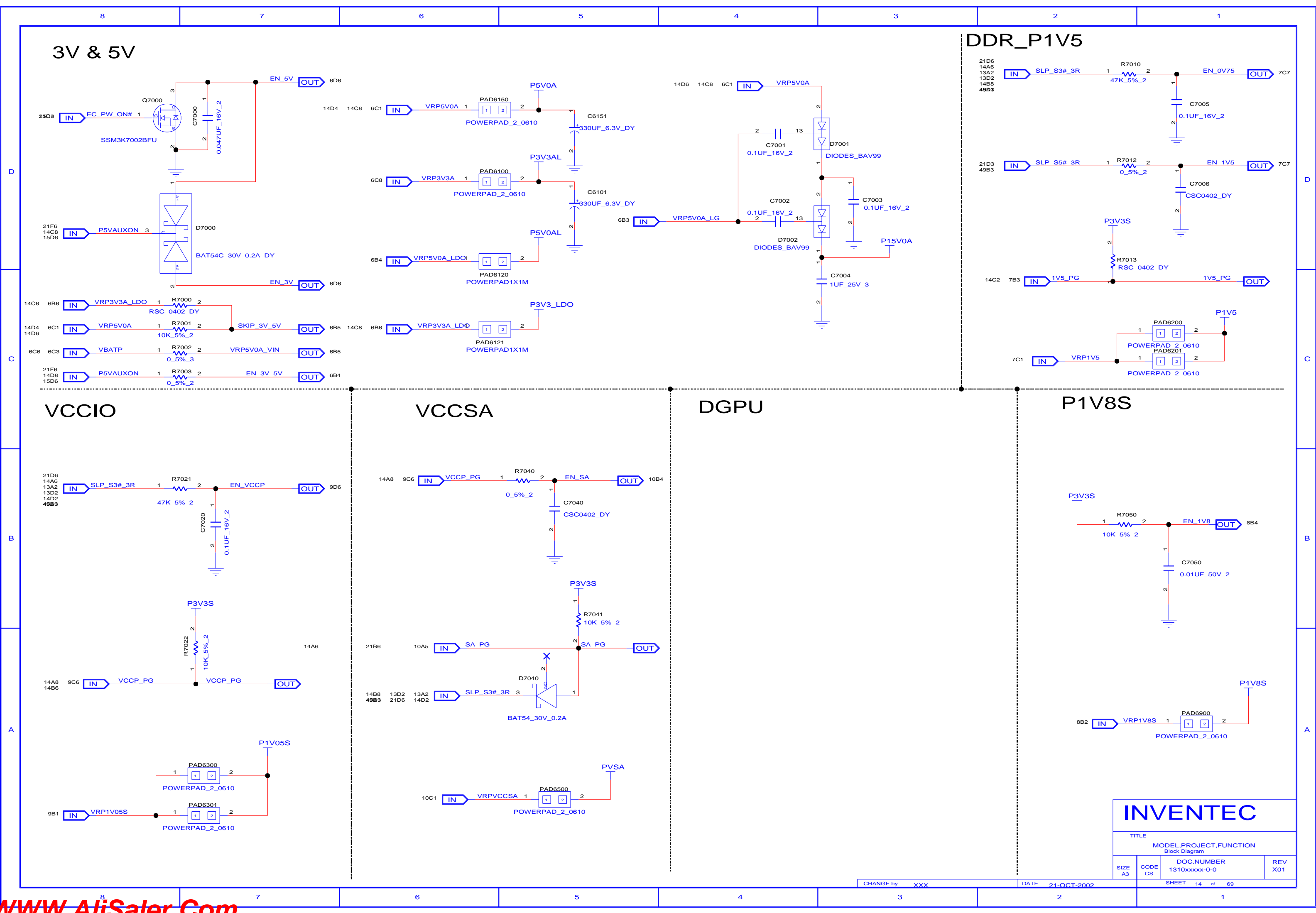
P.S. R6750(R1) R6751(R2) R6753(R3) R6754(R4)

RT8208A		RT8208B		Output Voltage Equation
G0	G1	G0	G1	
0	0	1	1	$V_{OUT} = \frac{R1+R2}{R2} \times 0.75$
1	0	0	1	$V_{OUT} = \frac{R1+(R2/R3)}{(R2/R3)} \times 0.75$
0	1	1	0	$V_{OUT} = \frac{R1+(R2/R4)}{(R2/R4)} \times 0.75$
1	1	0	0	$V_{OUT} = \frac{R1+(R2/R3/R4)}{(R2/R3/R4)} \times 0.75$

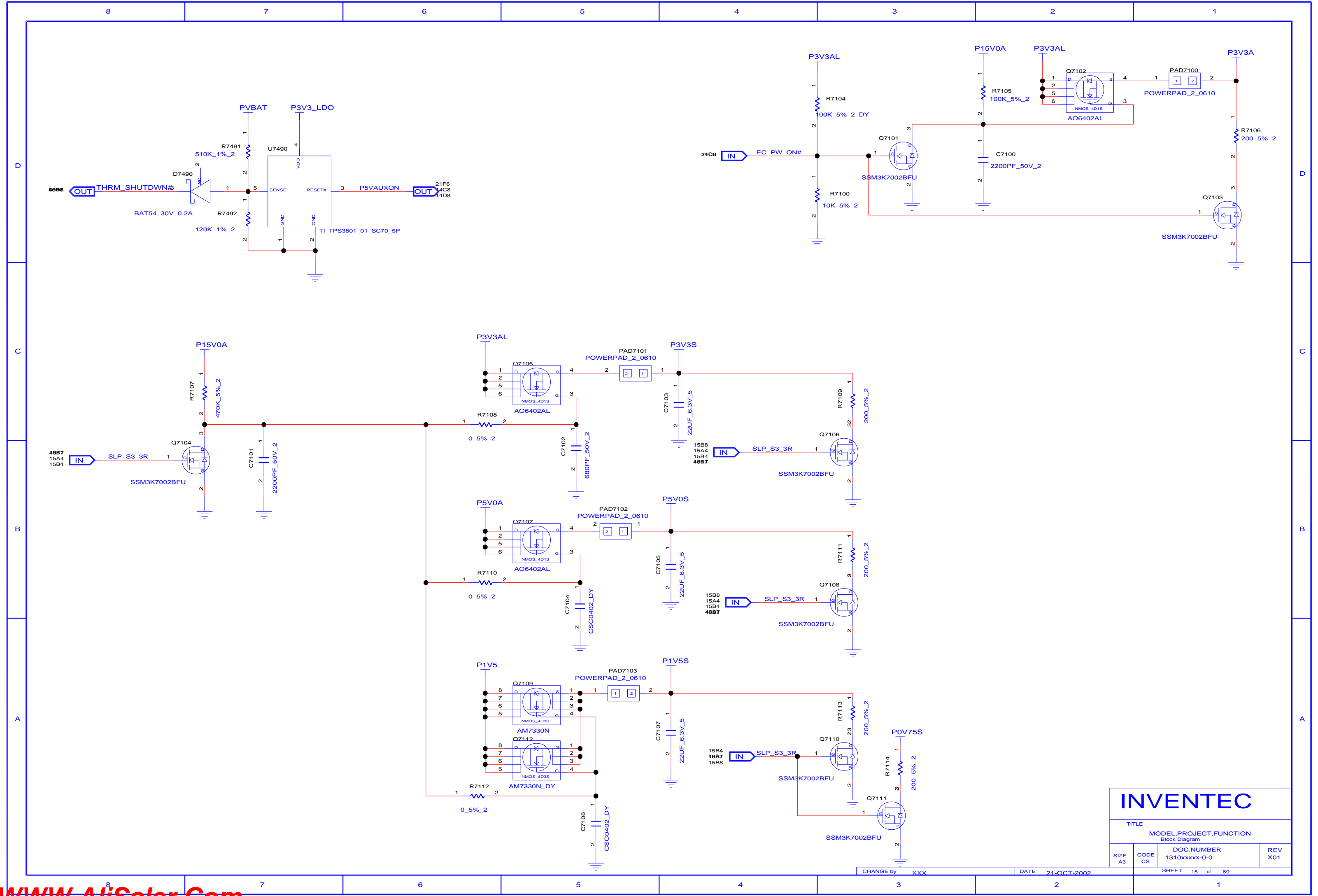


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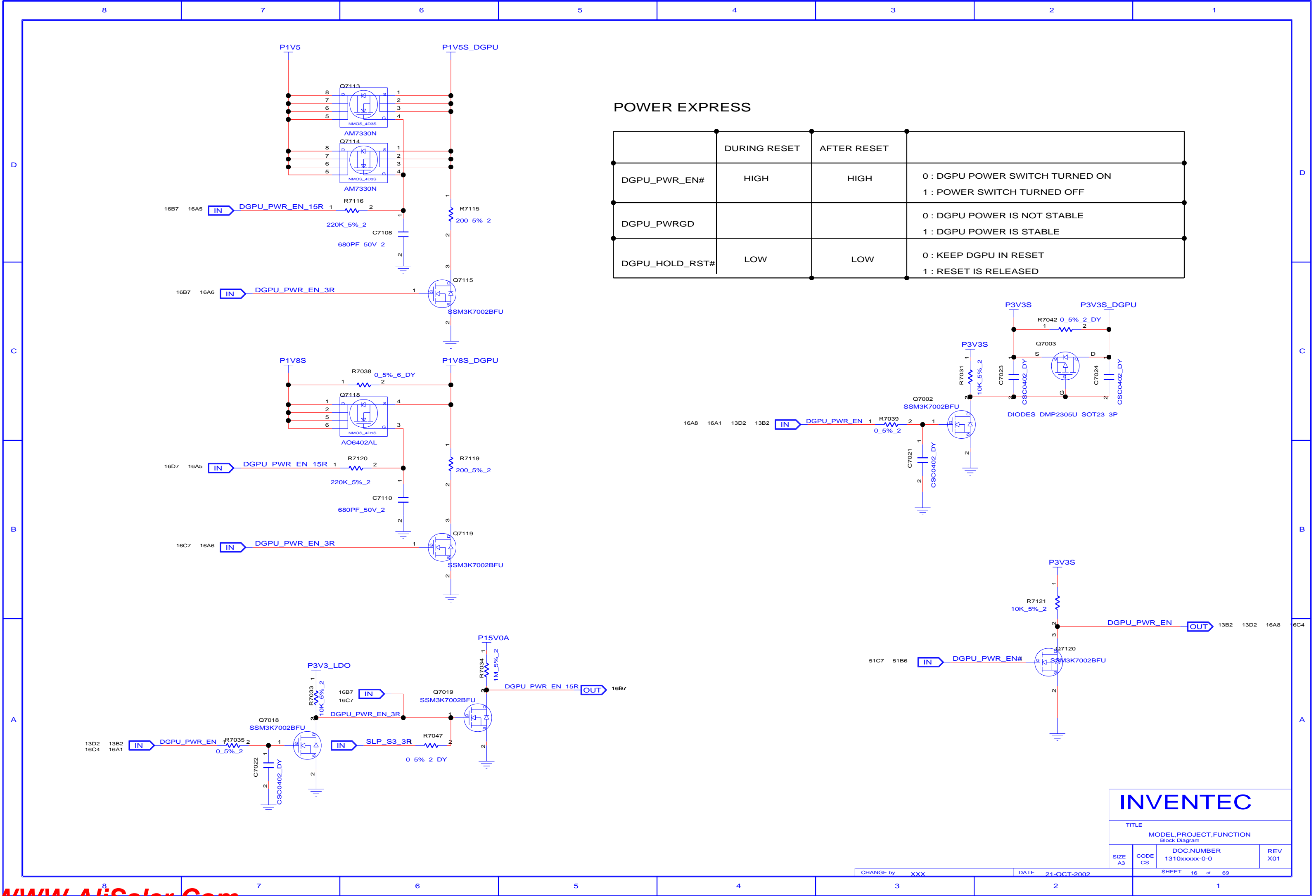
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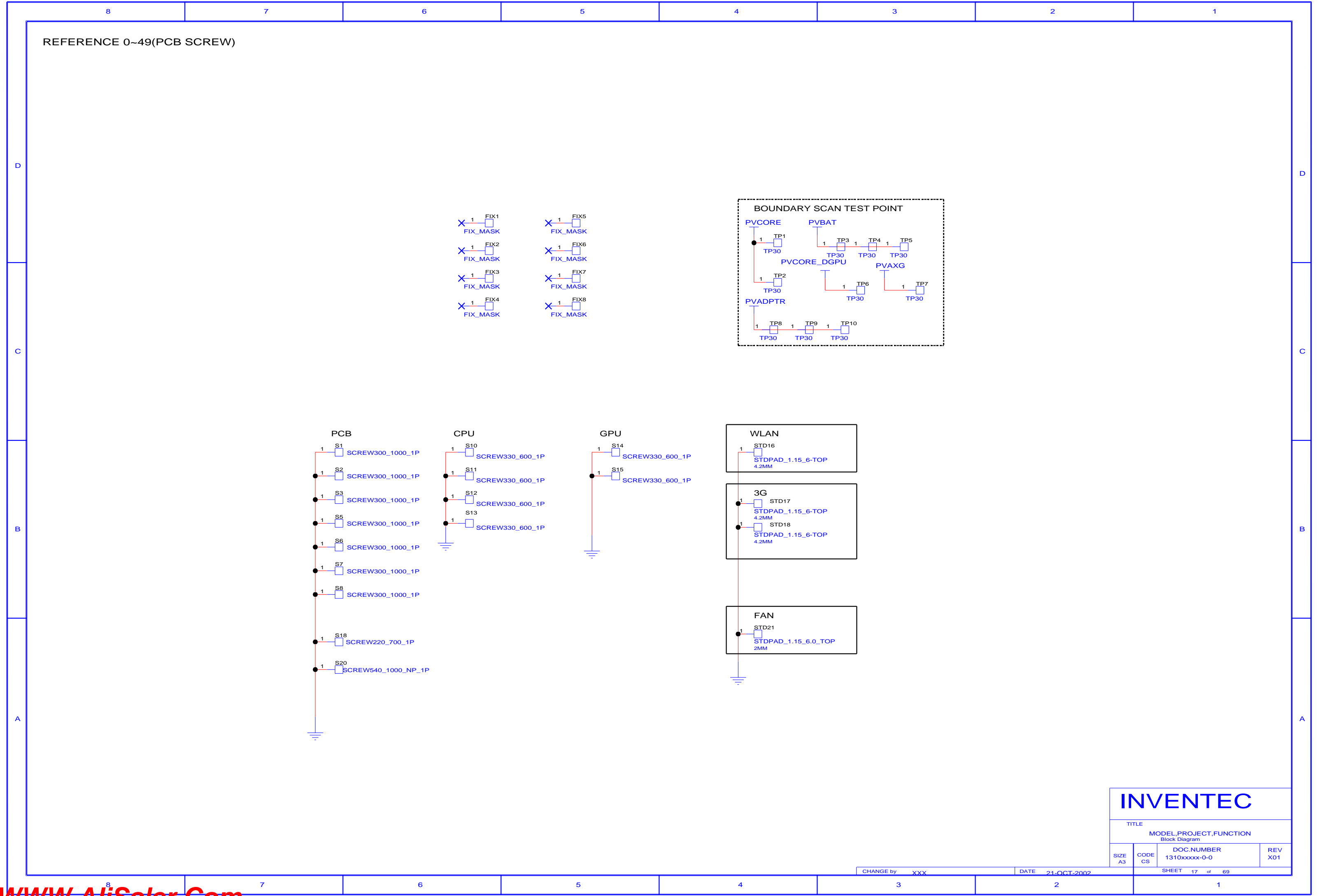
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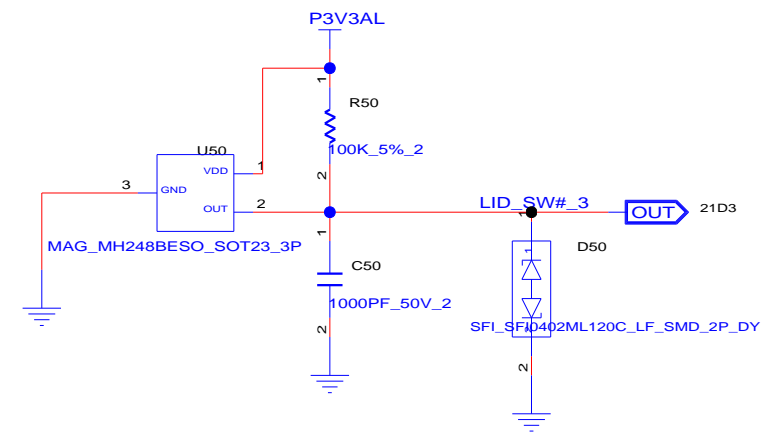
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REFERENCE 50-99(HALL SENSOR)

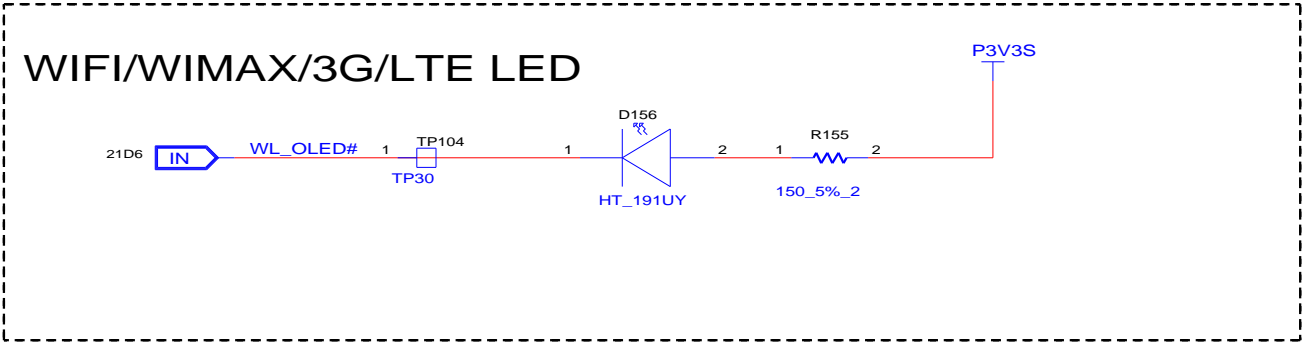
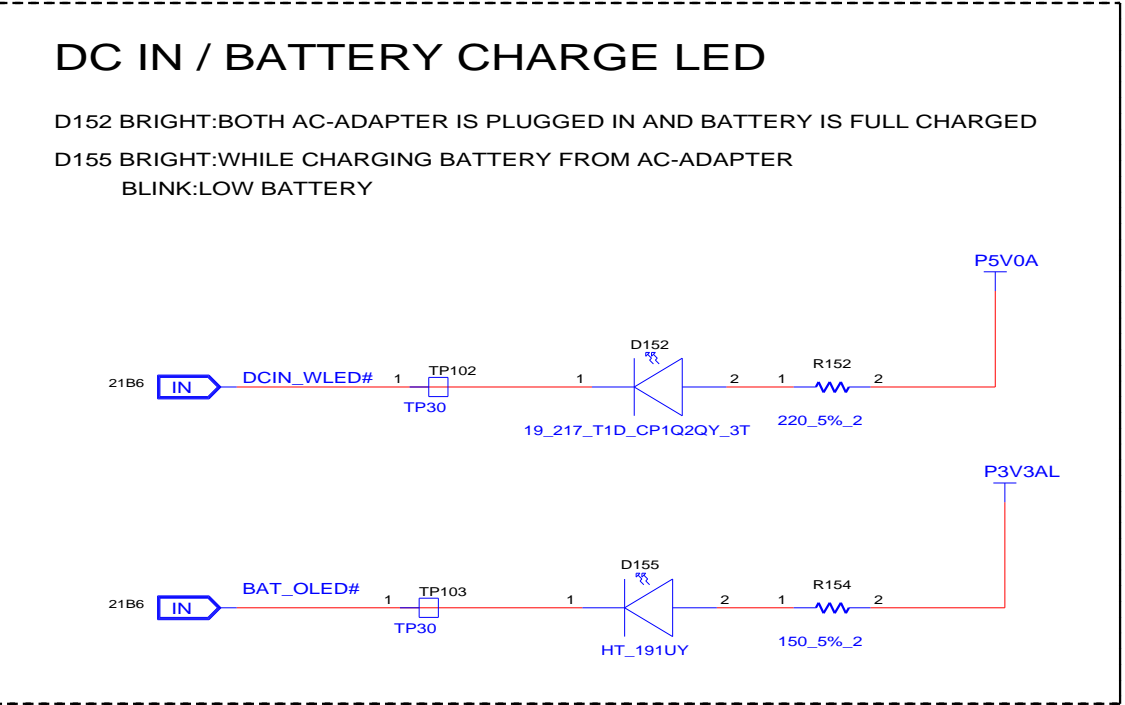
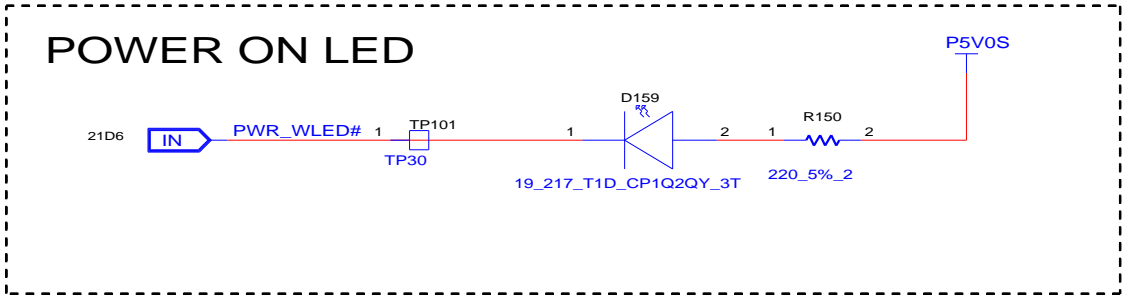
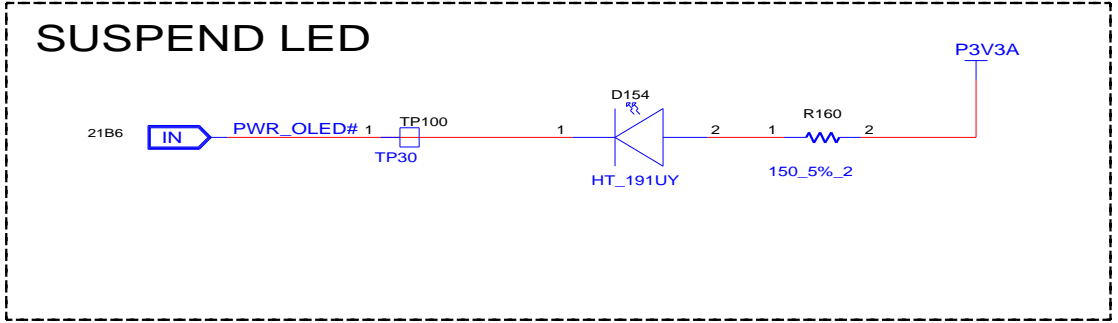


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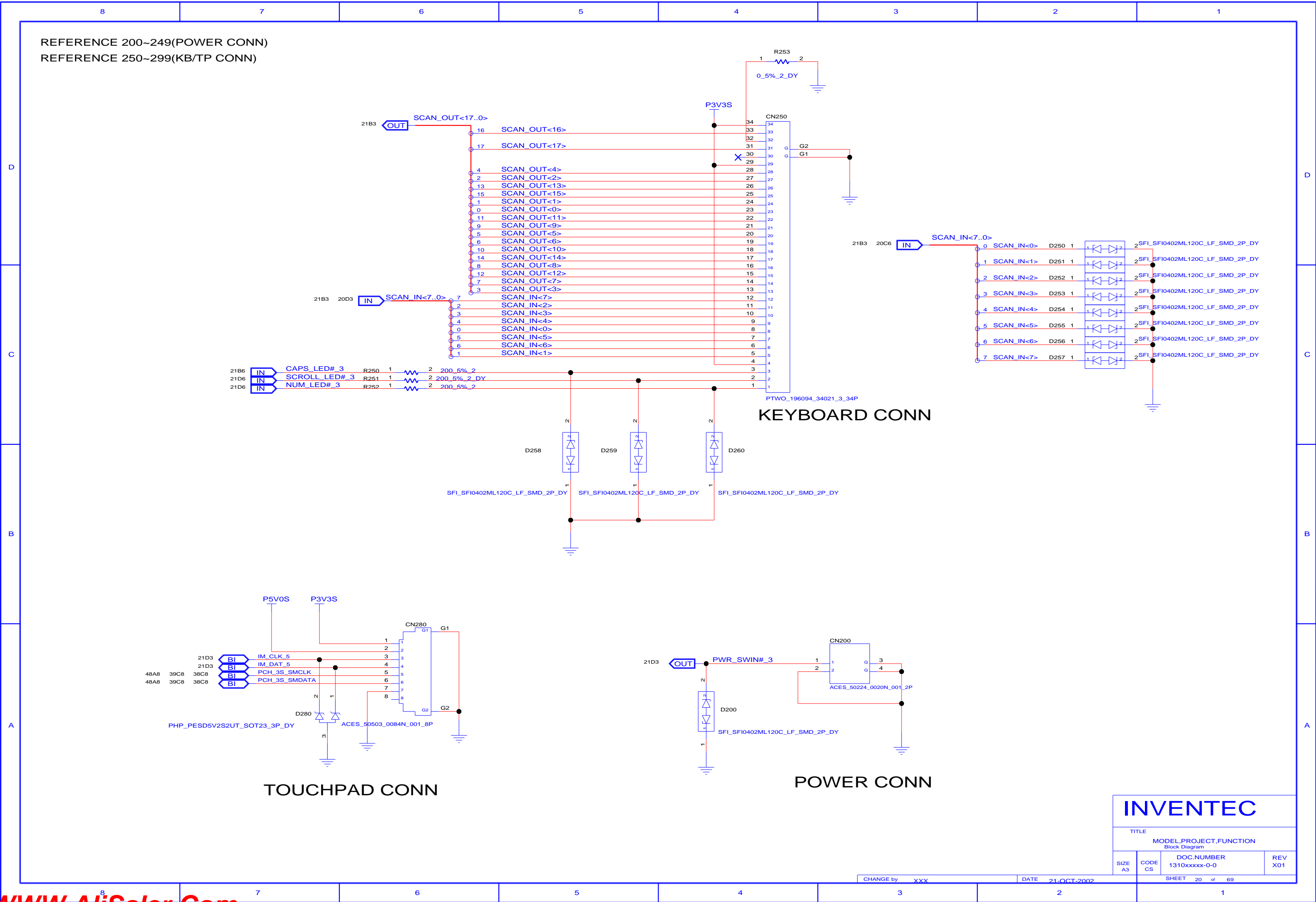
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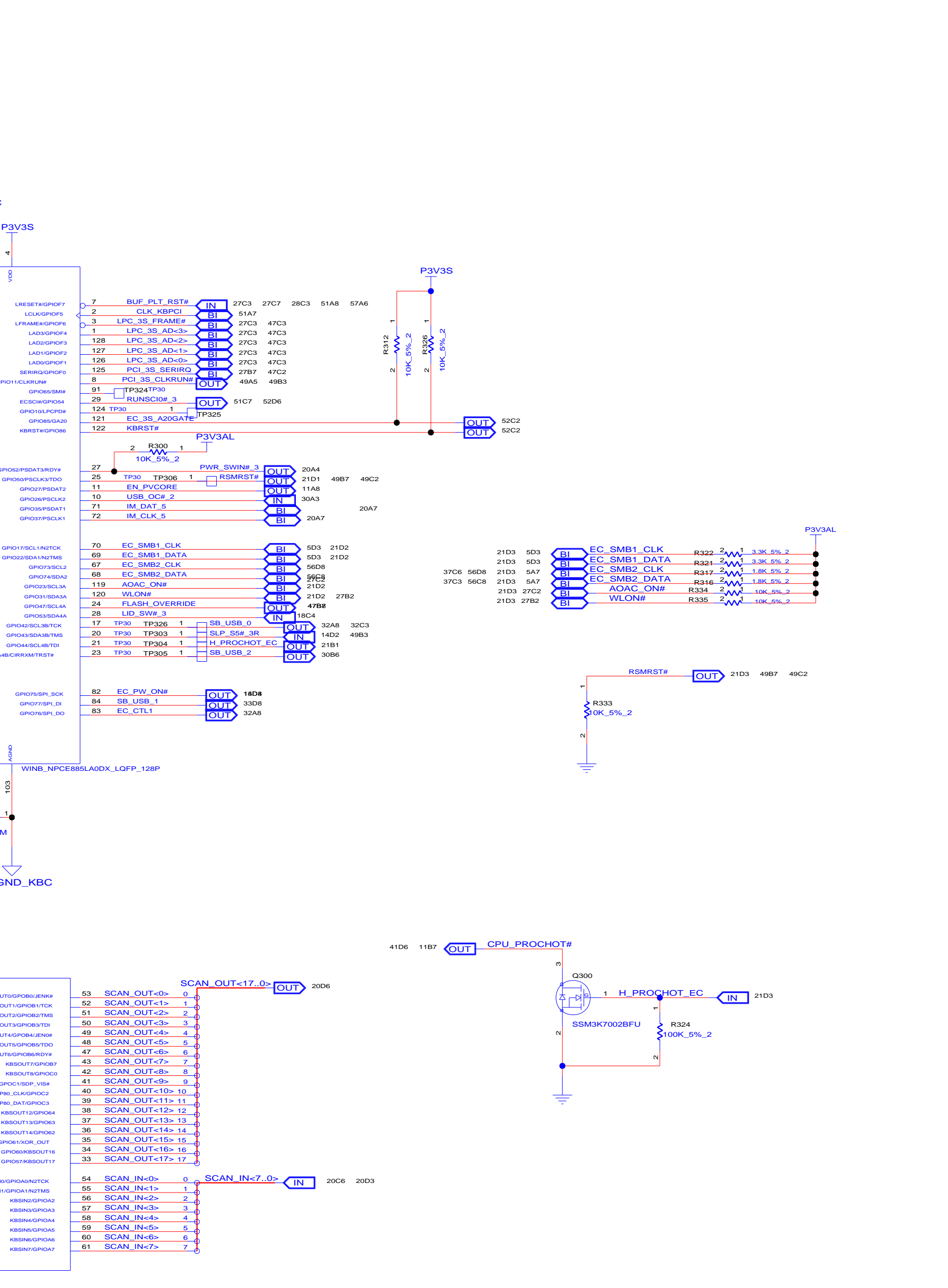
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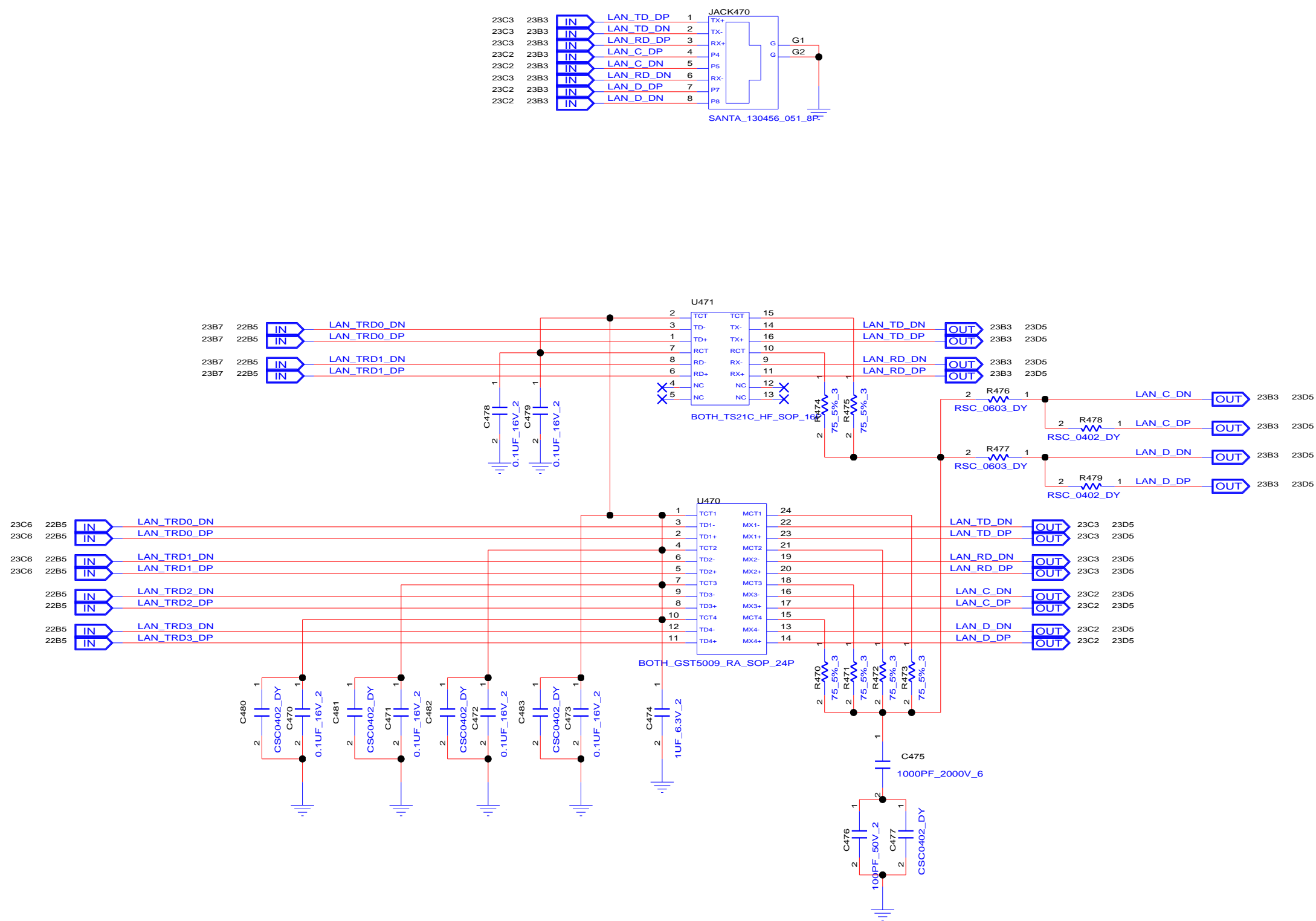


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REFERENCE 400~499(LAN)



INVENTEC

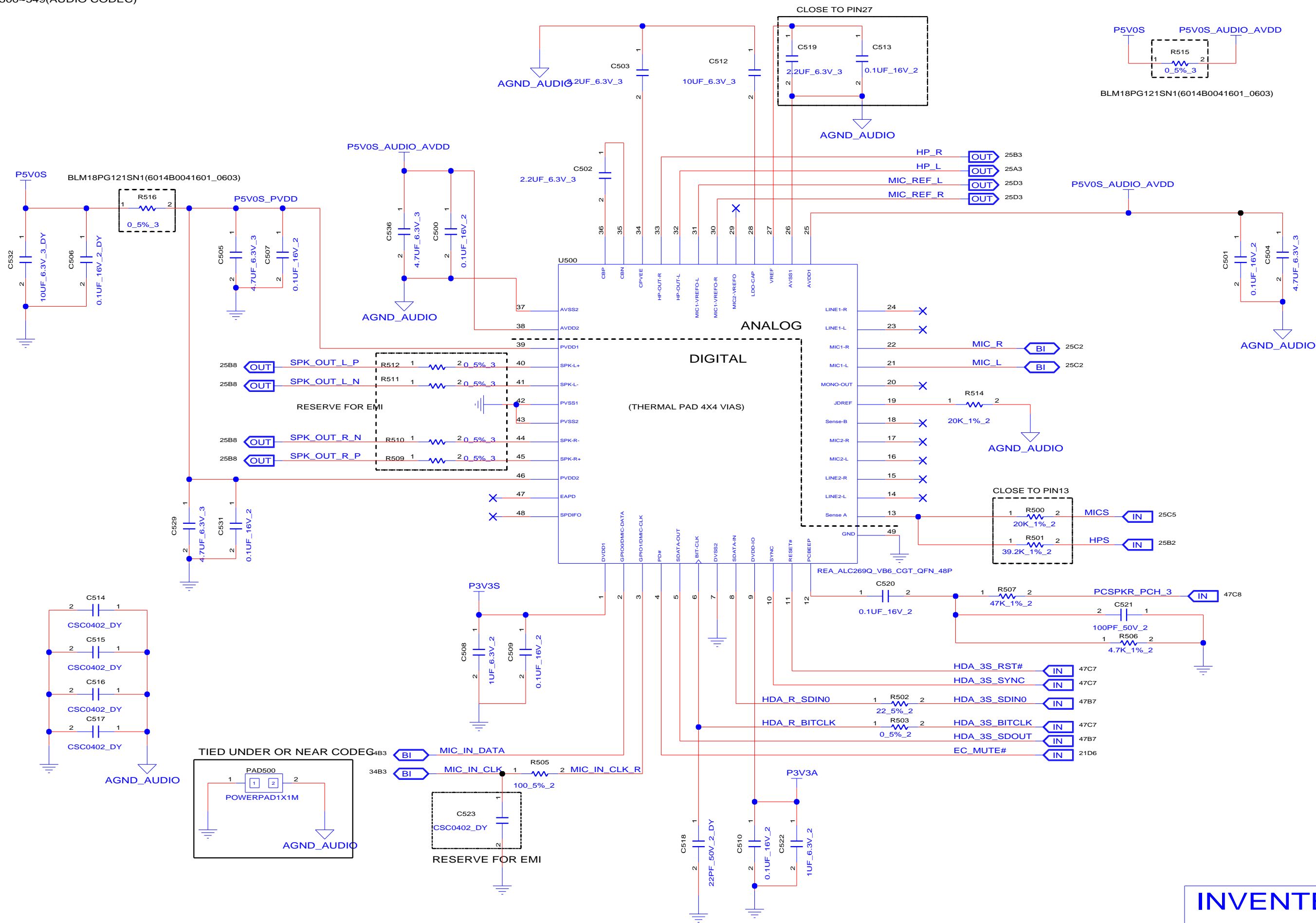
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REFERENCE 500~549(AUDIO CODEC)



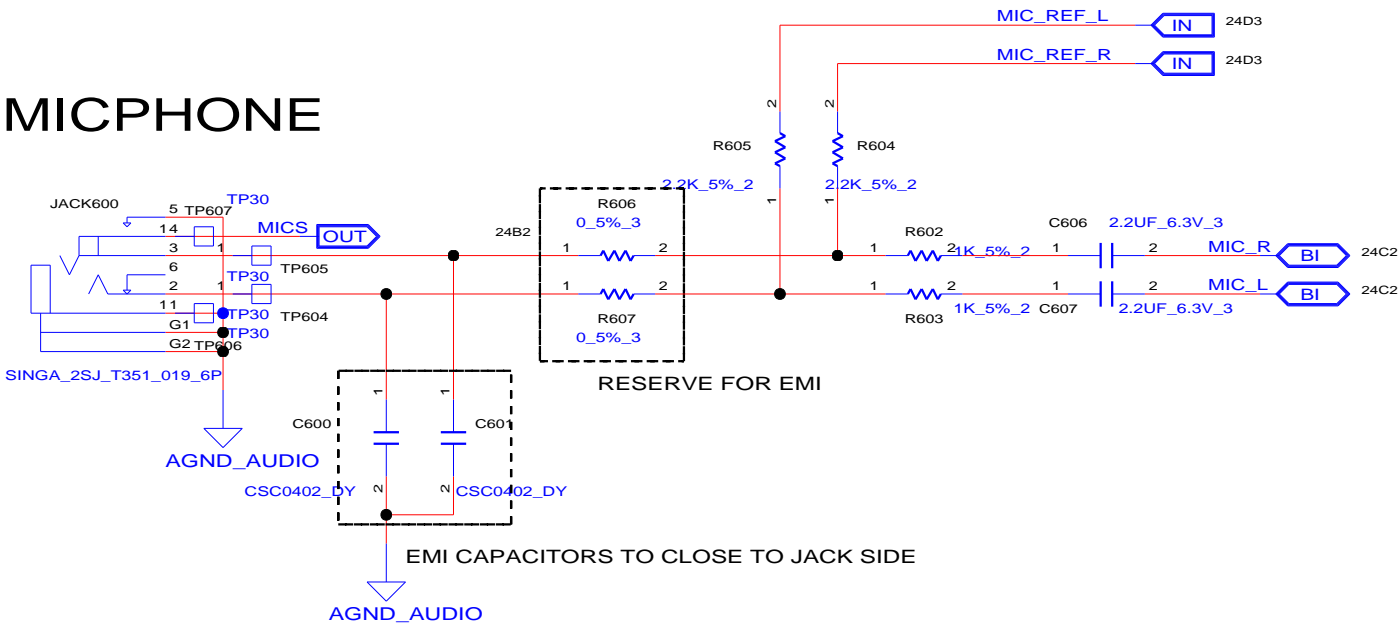
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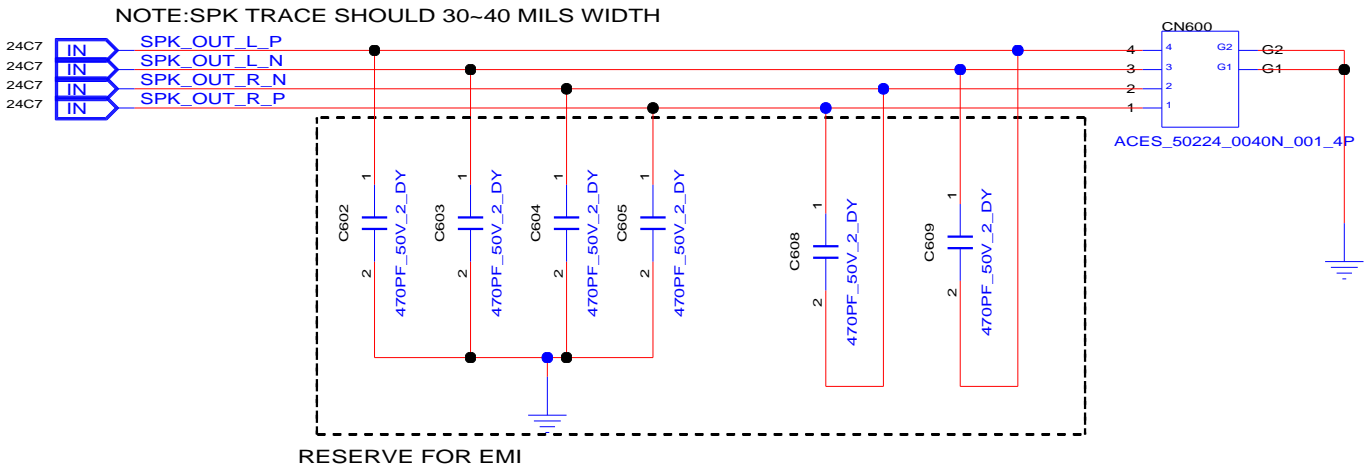
REFERCE 600~649(JACK/MIC/SPEAKER)

# AUDIO JACKS

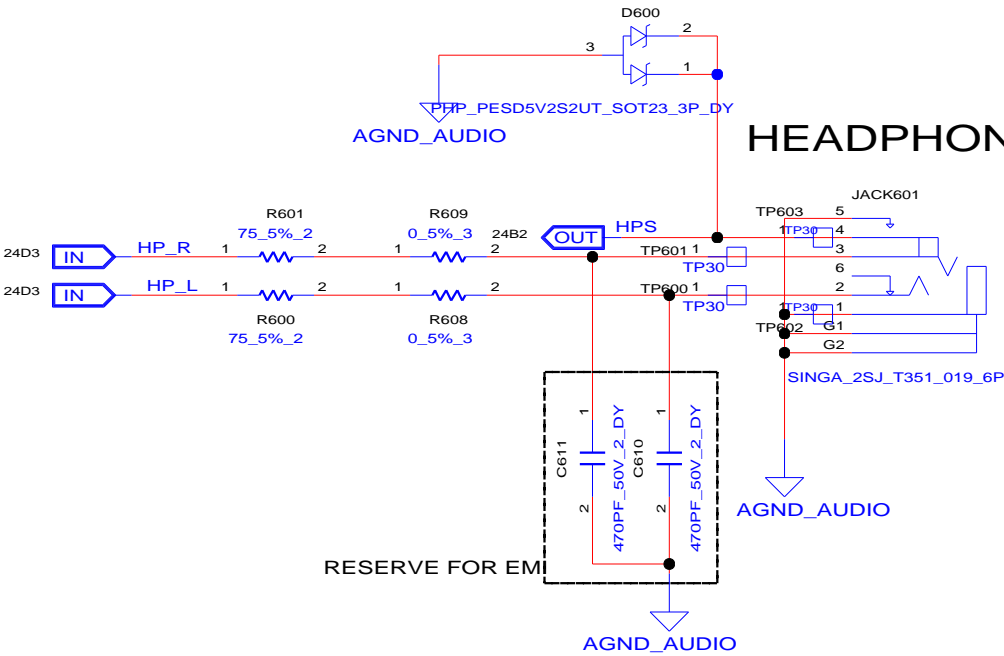
## MICPHONE



## INTERNAL SPEAKERS



## HEADPHONE



INVENTEC

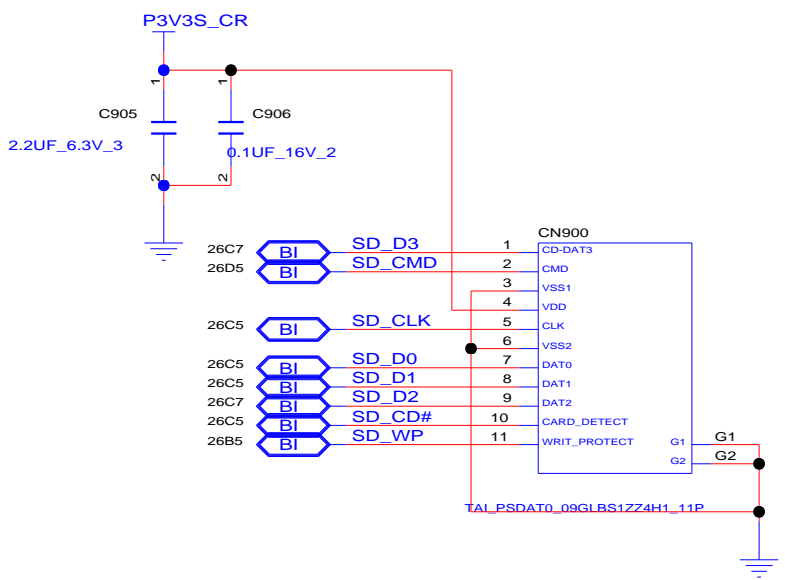
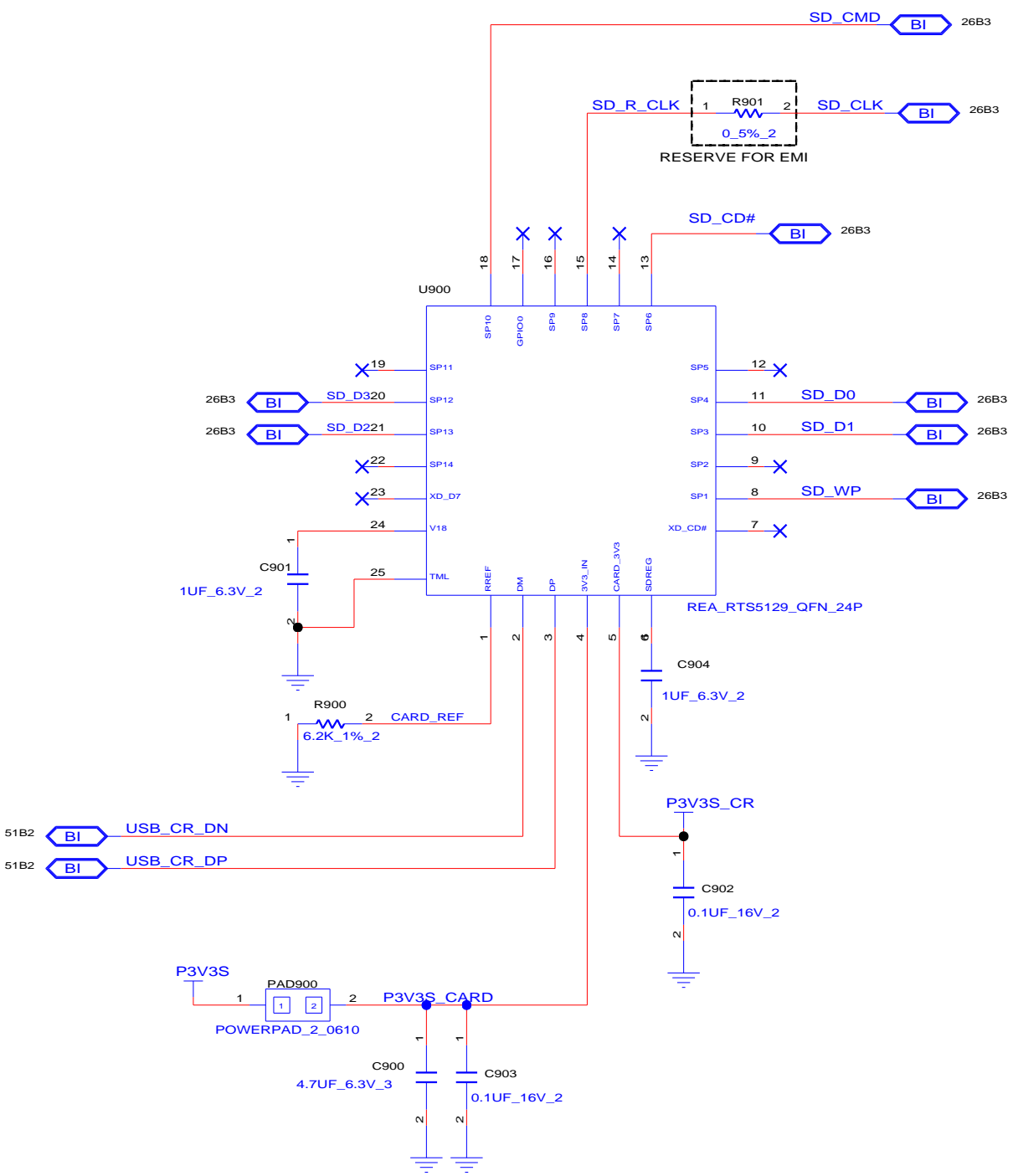
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REFERNCE 900~999(CARDREADER)



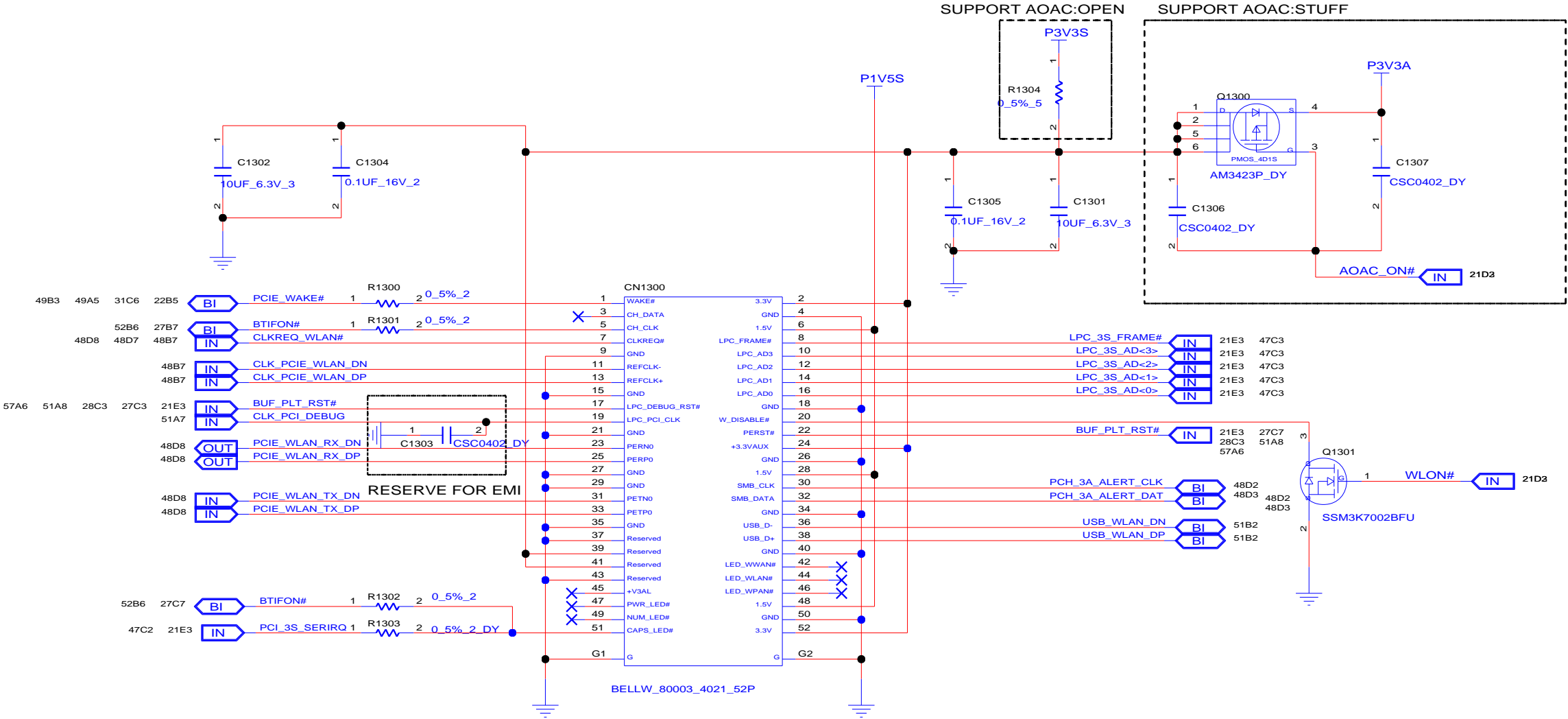
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REFERENCE 1300~1349(WLAN)

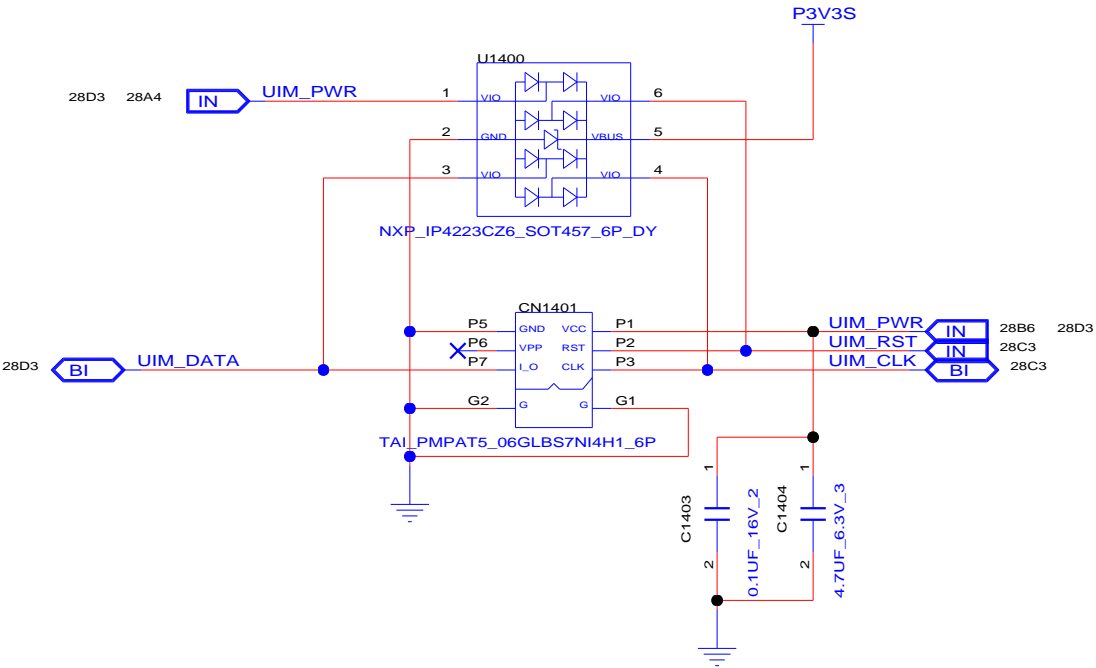
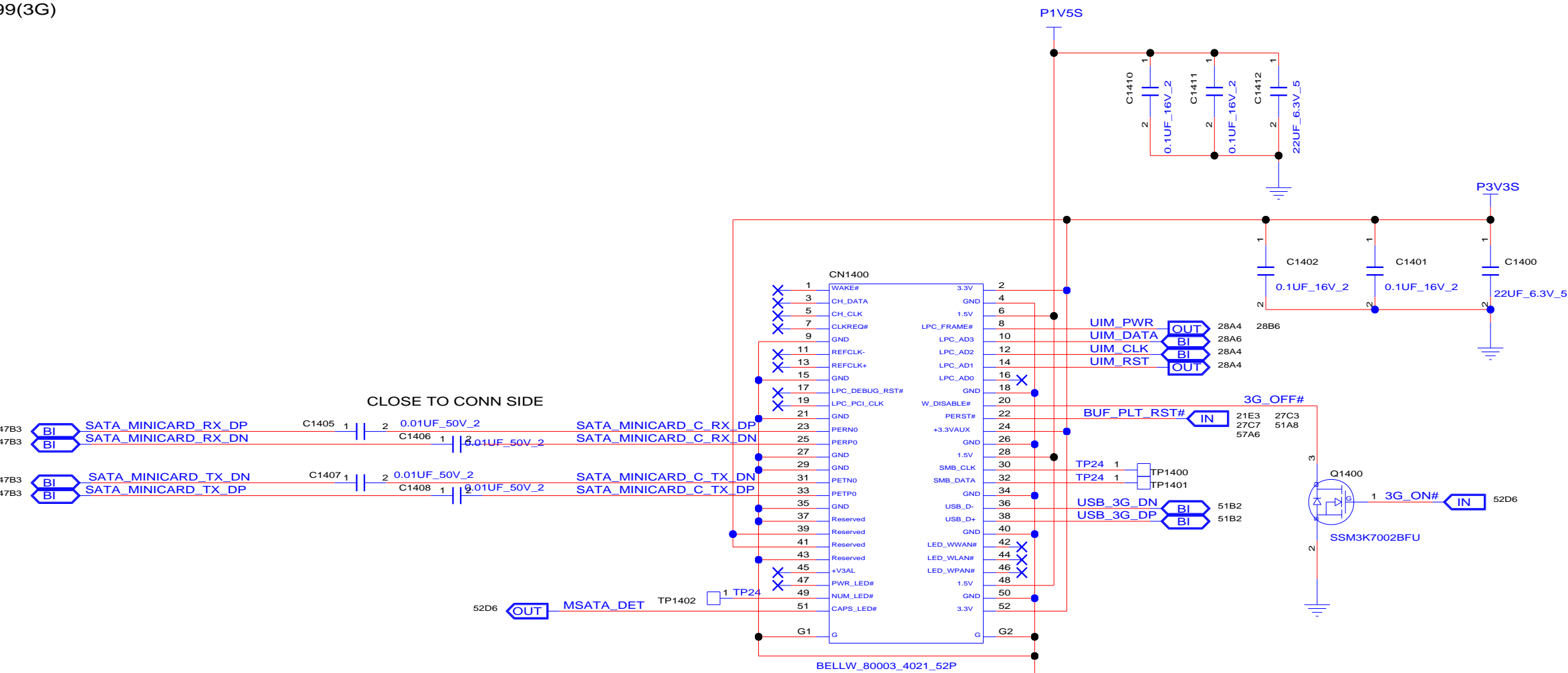


MINI CARD 1(WLAN)

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REFERENCE 1400~1499(3G)



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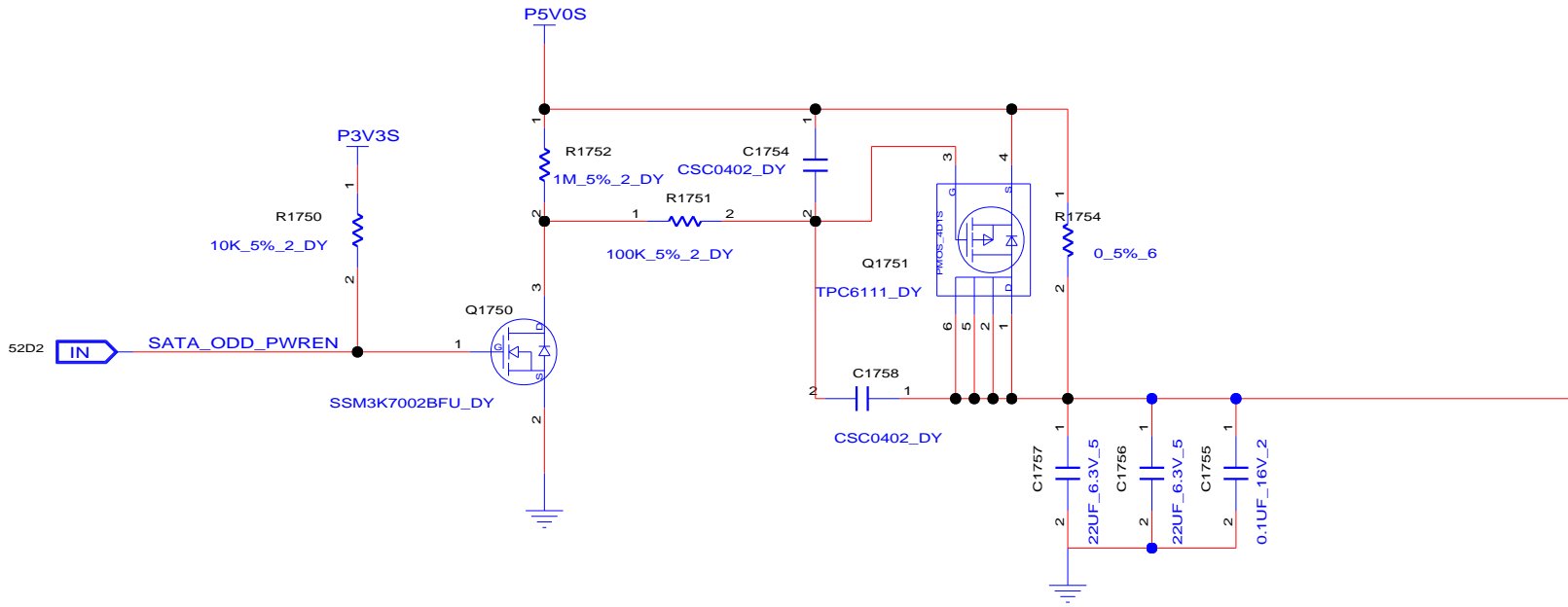
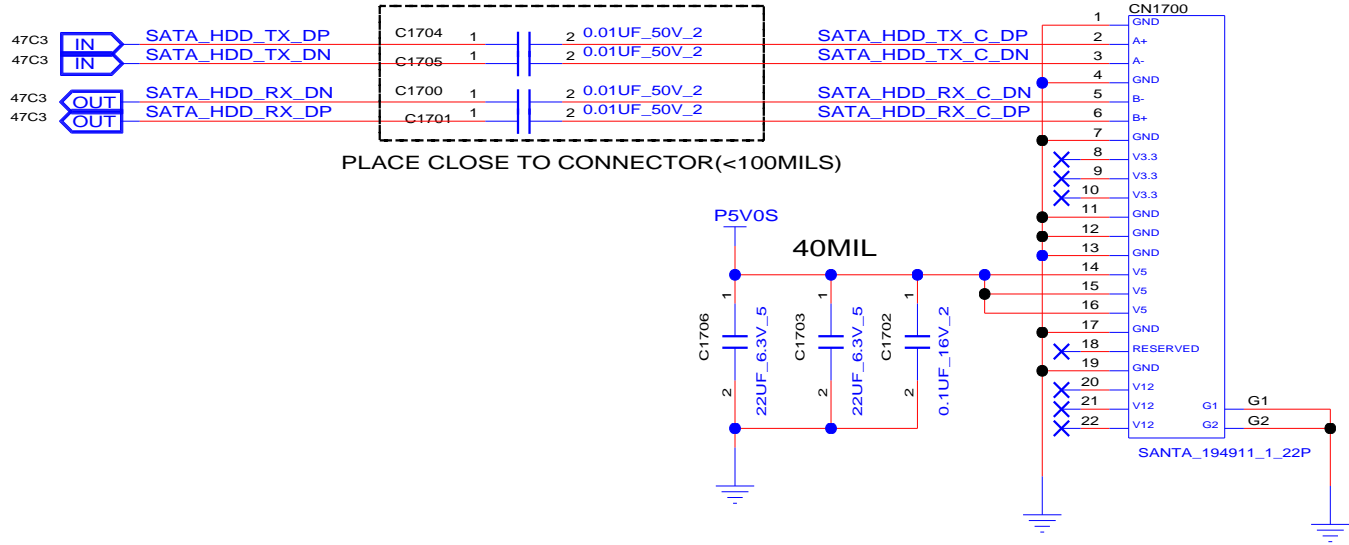
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REFERENCE 1700~1749(HDD)  
REFERENCE 1750~1799(ODD)

# SATA HDD



# SATA ODD

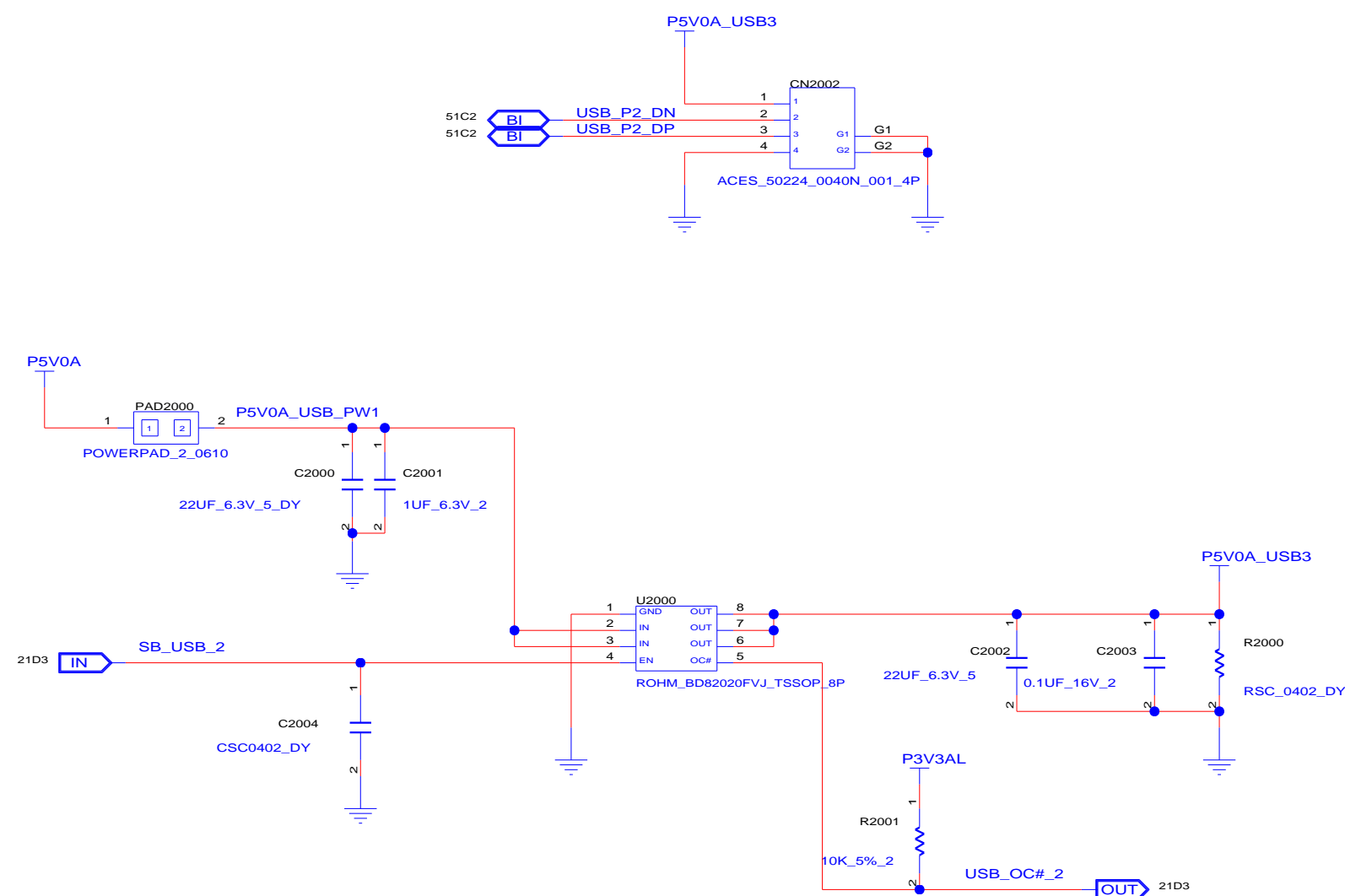
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REFERENCE 2000~2099(USB)



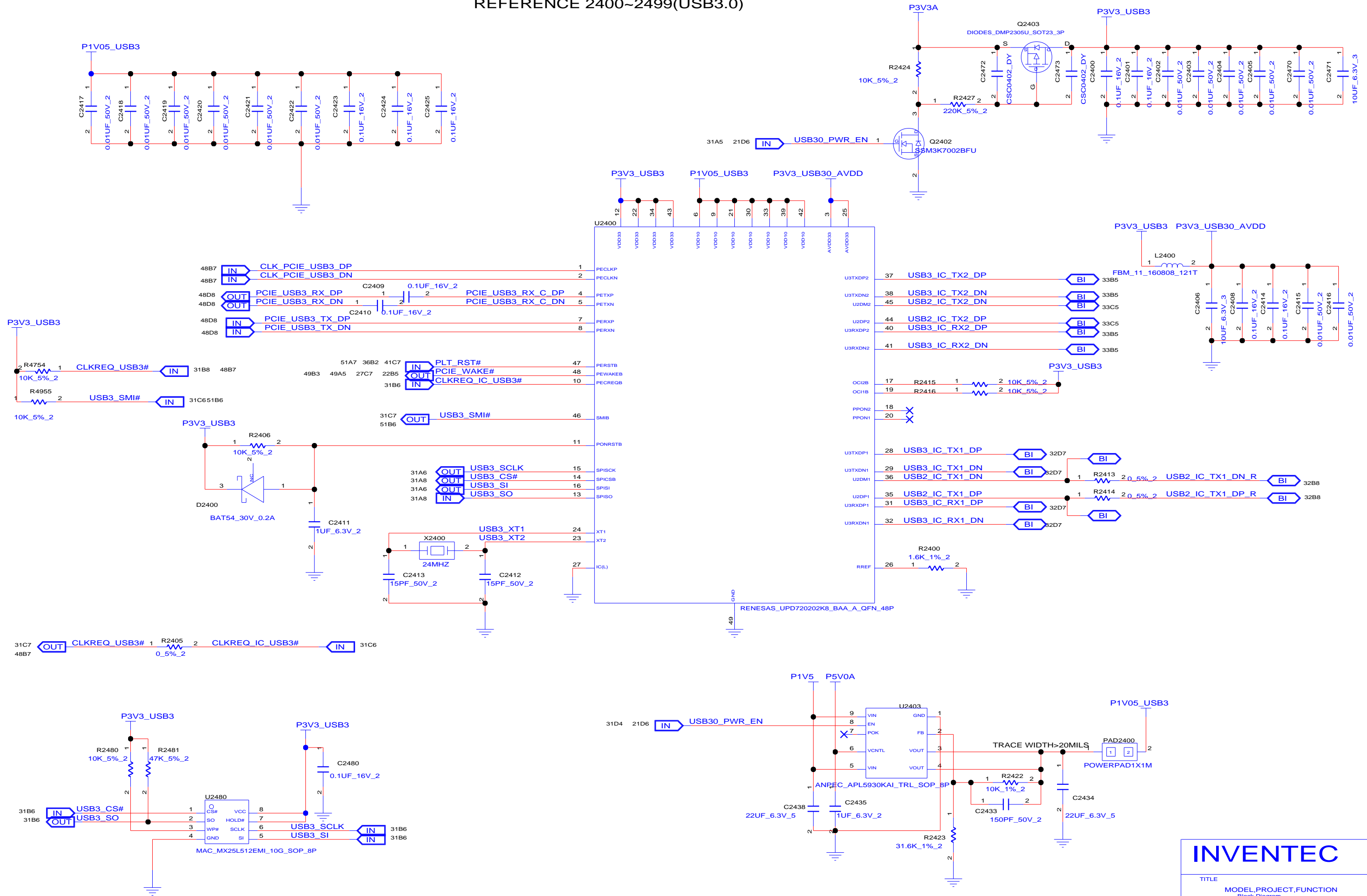
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## REFERENCE 2400~2499(USB3.0)

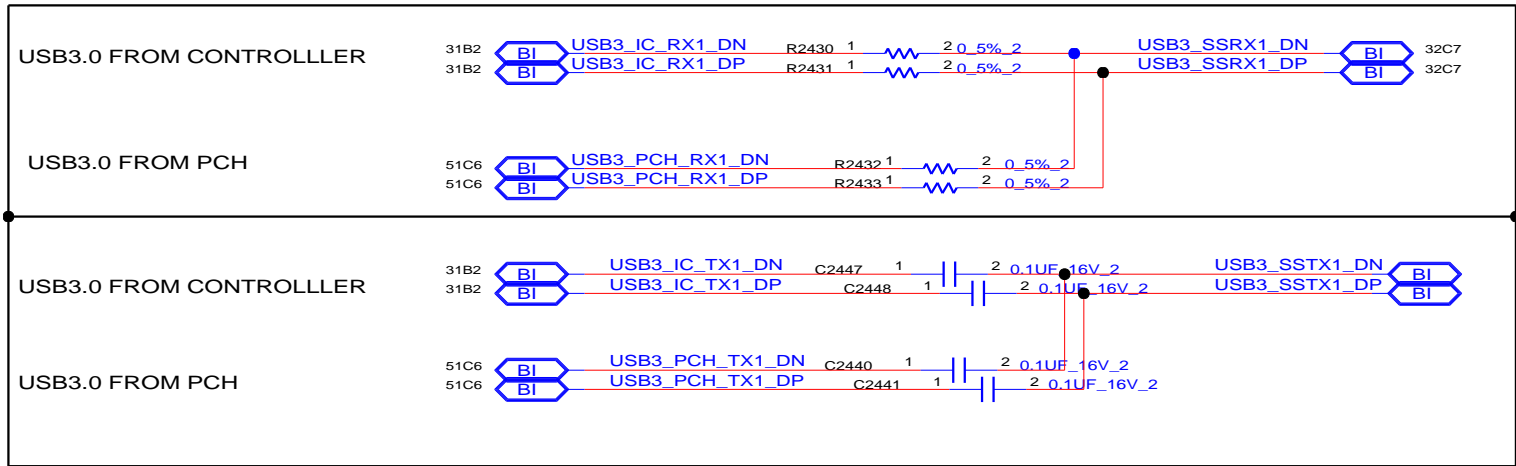


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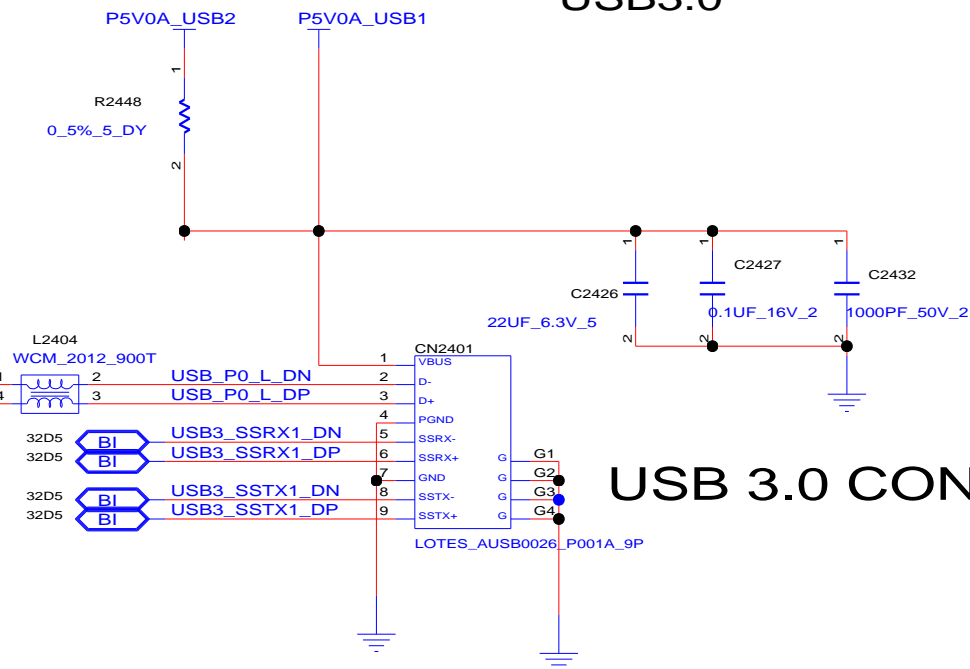
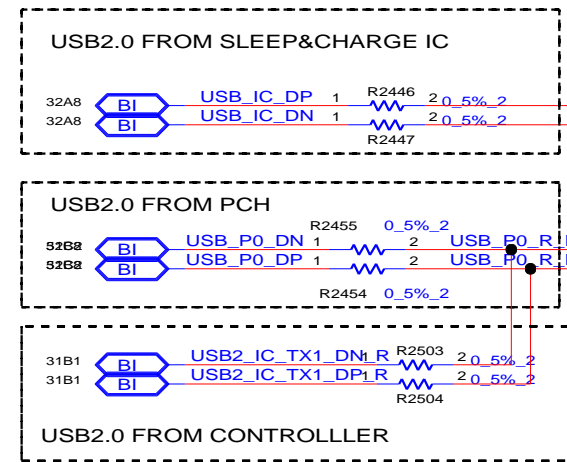
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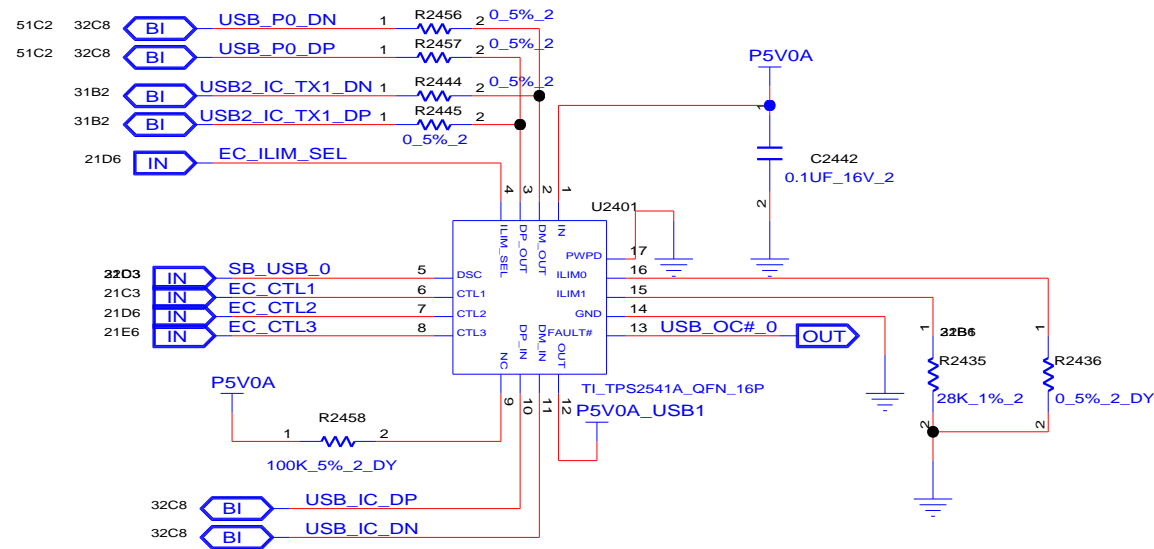
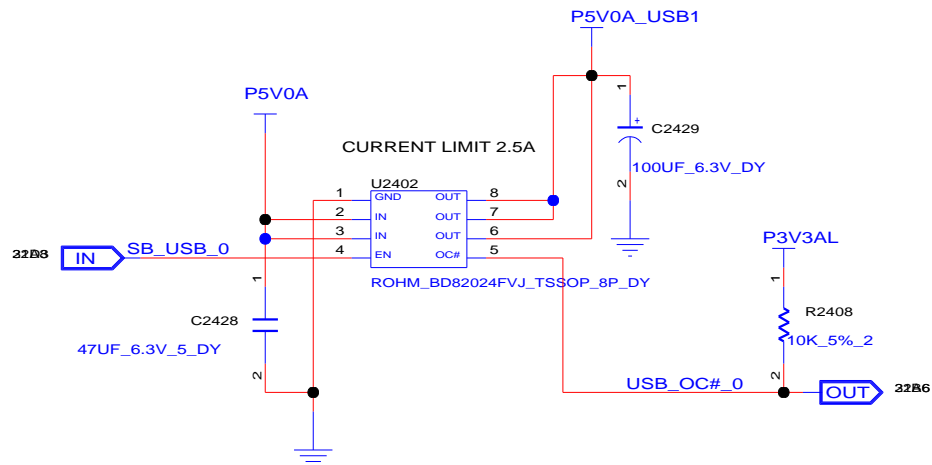
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USB3.0



USB 3.0 CONNECTOR



CTL1	CTL2	CTL3	Mode
0	0	X	Dedicated Charging Port, Auto-detect
0	1	X	Dedicated Charging Port, BC Specification 1.1 Only
1	0	X	Dedicated Charging Port, Apple Only
1	1	0	Standard Downstream Port, USB 2.0 Mode
1	1	1	Charging Downstream Port, BC Specification 1.1

Table 3 – TPS2541 Control Truth Table

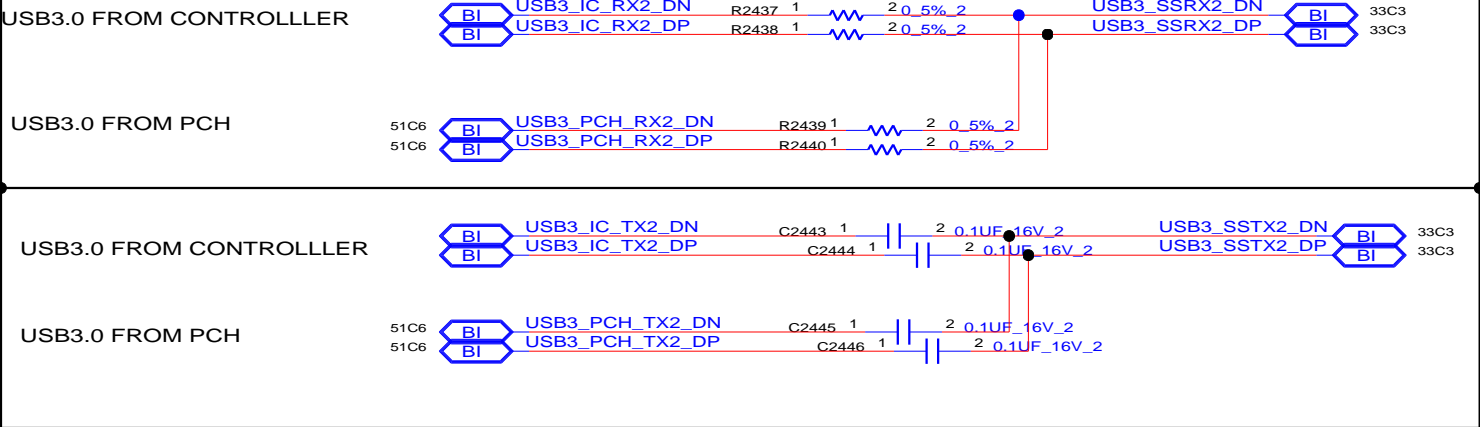
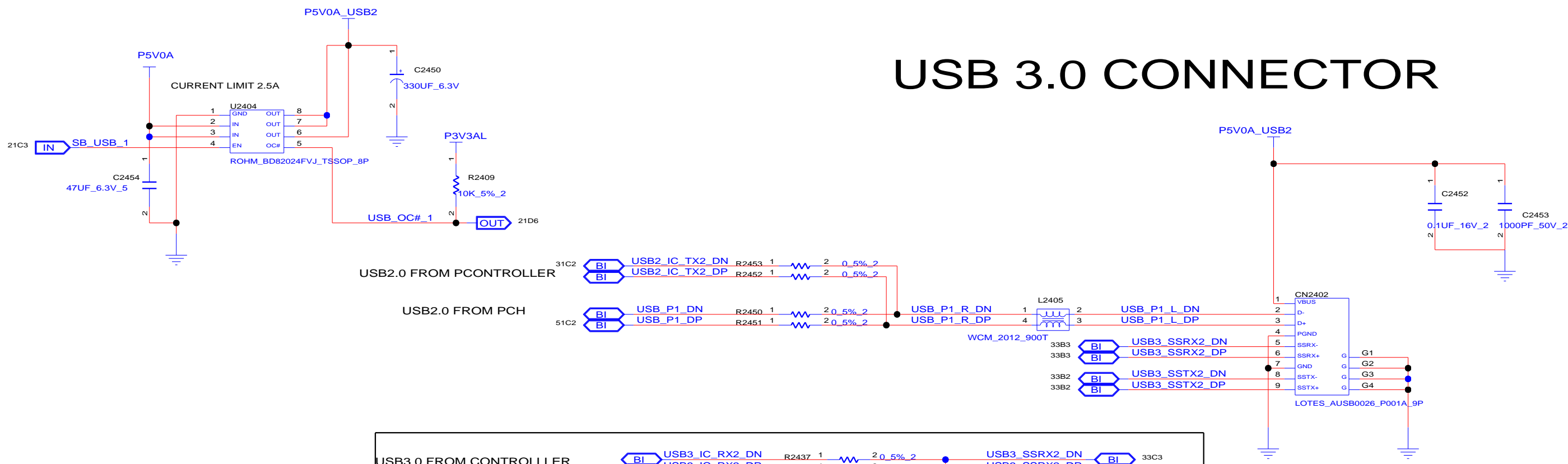
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REFERENCE 2400~2499(USB3.0)

# USB 3.0 CONNECTOR

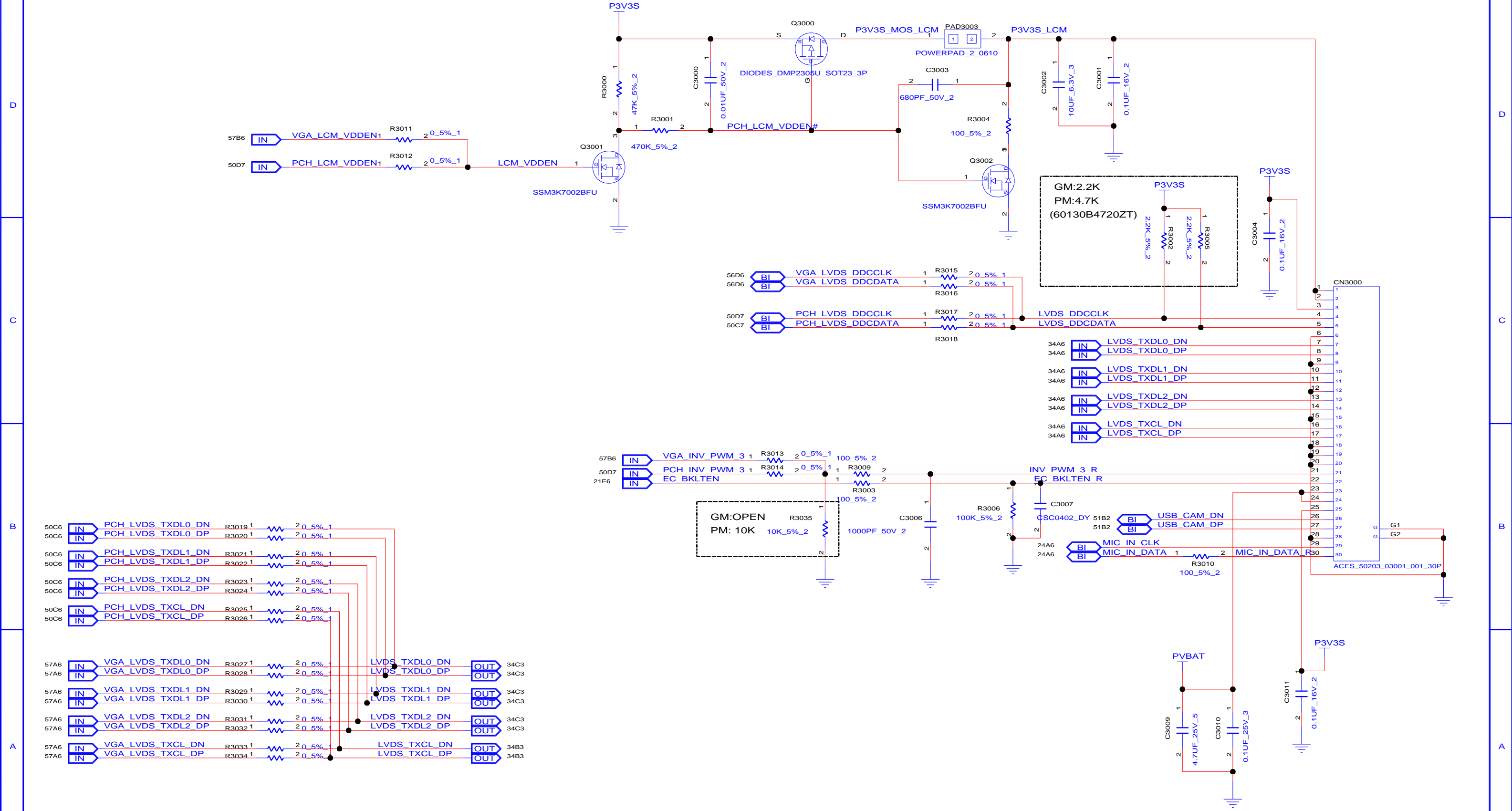


USB3.0

INVENTEC

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REFERENCE 3000~3049(LCM)



INVENTEC

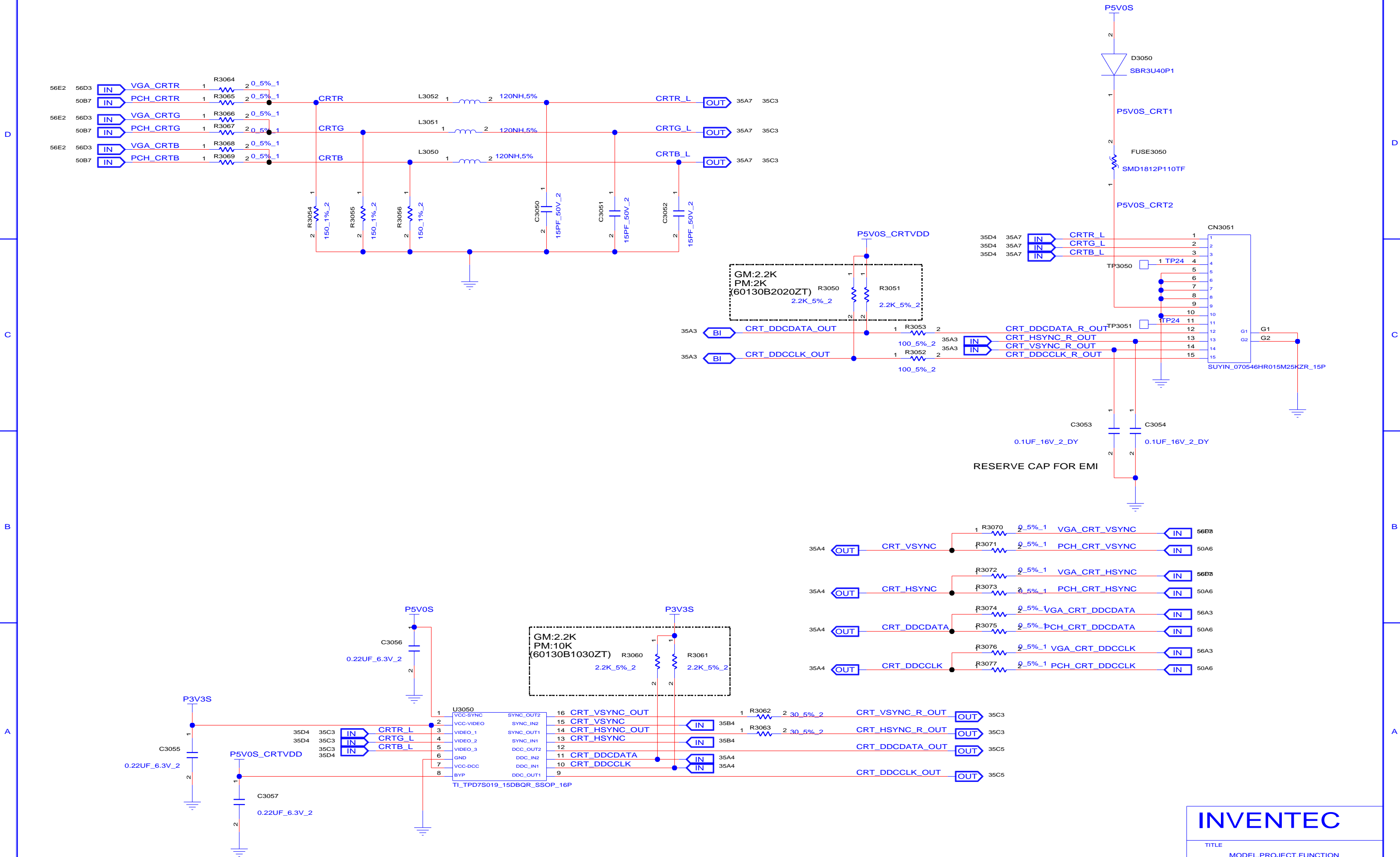
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REFERENCE 3050~3099(CRT)



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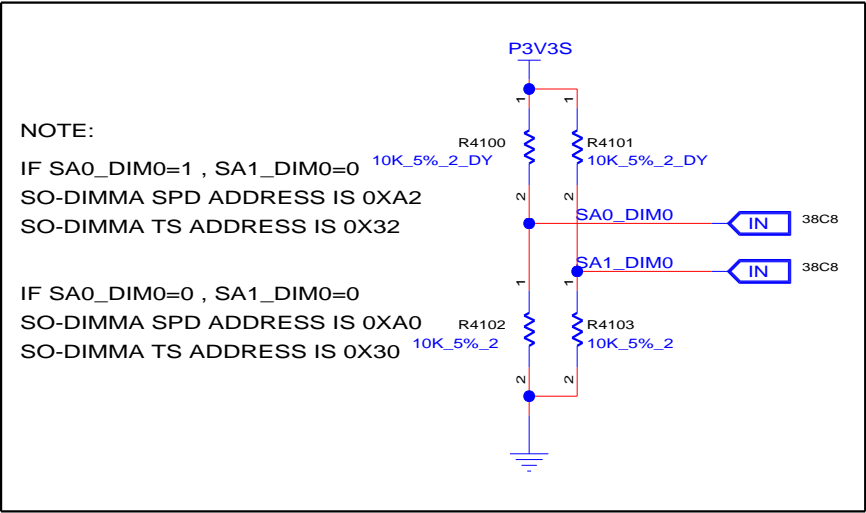
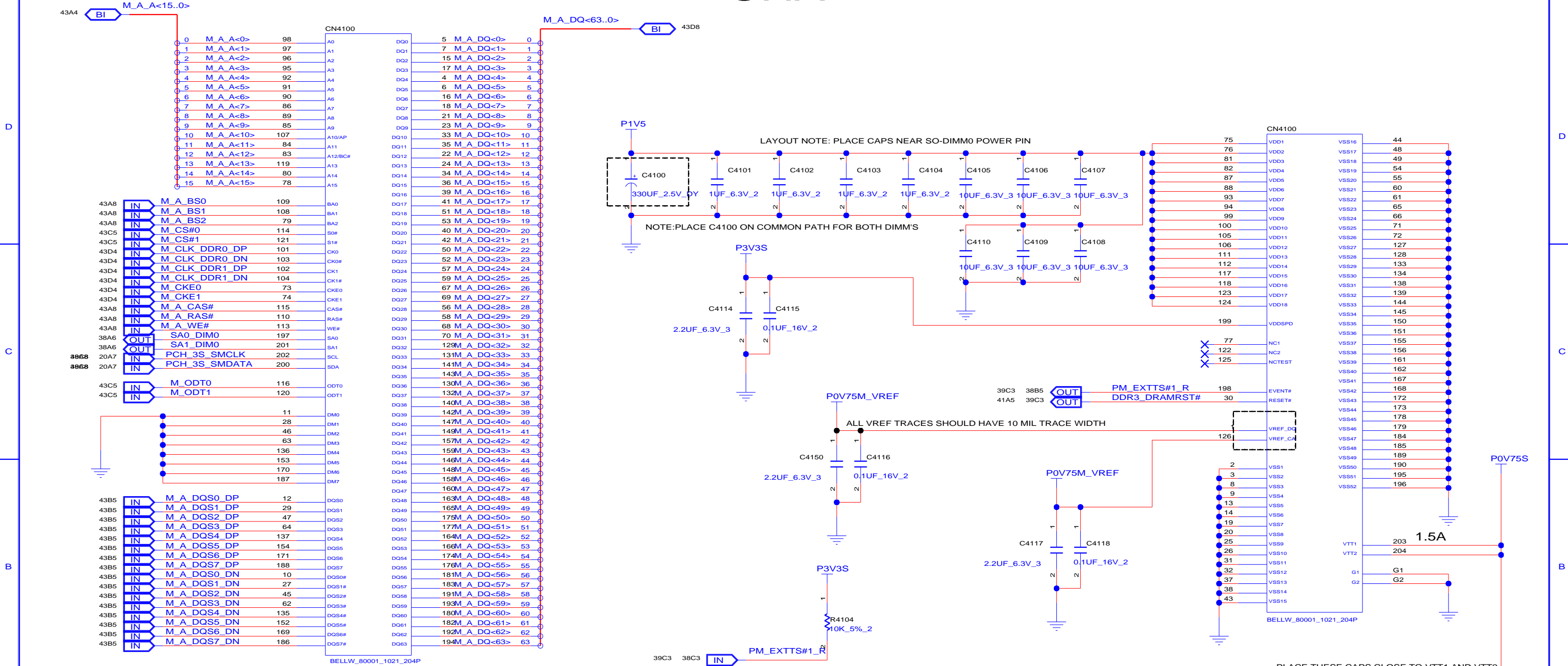


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CHA

REFERENCE 4100~4299(DDR)



INVENTEC

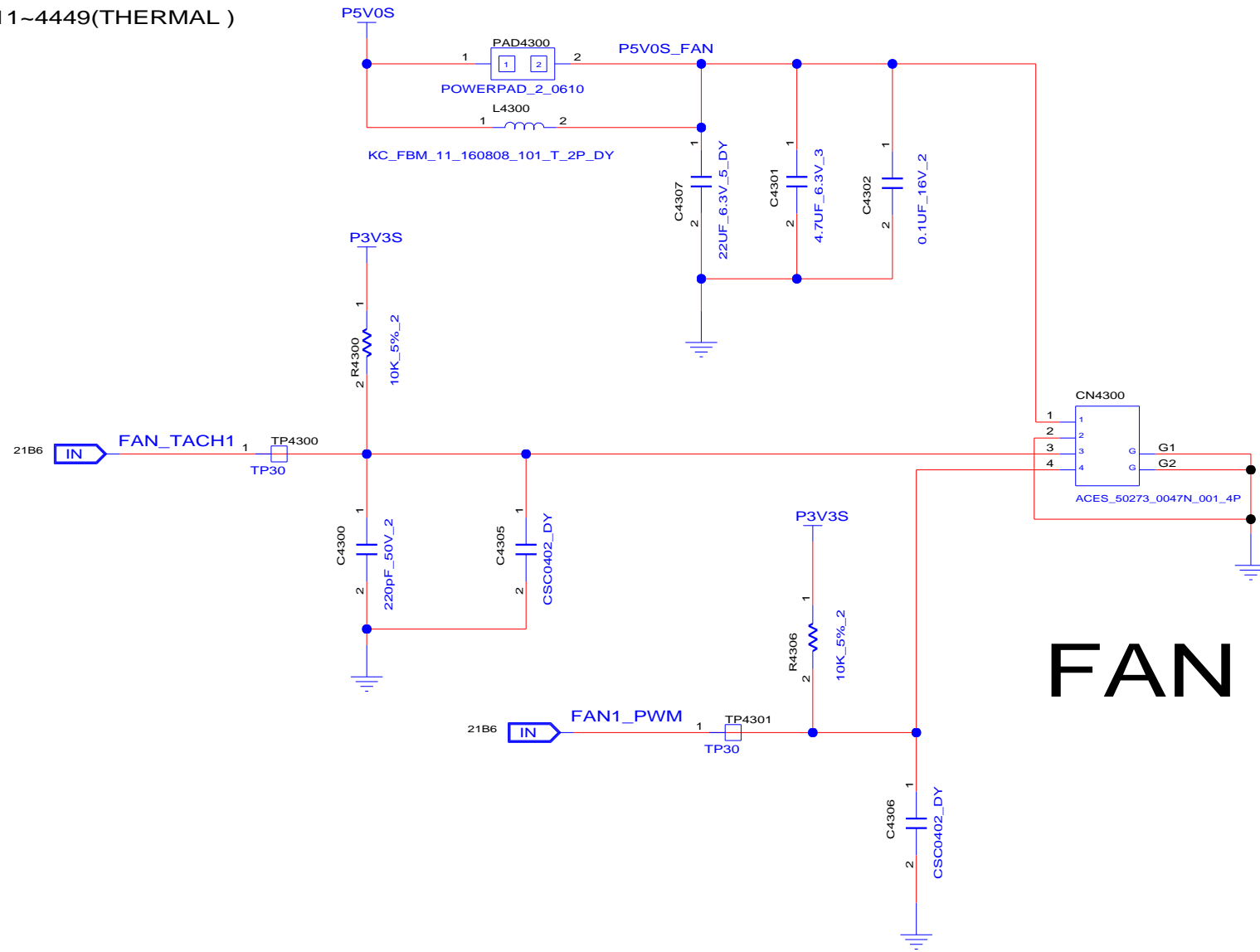
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 38 of 69

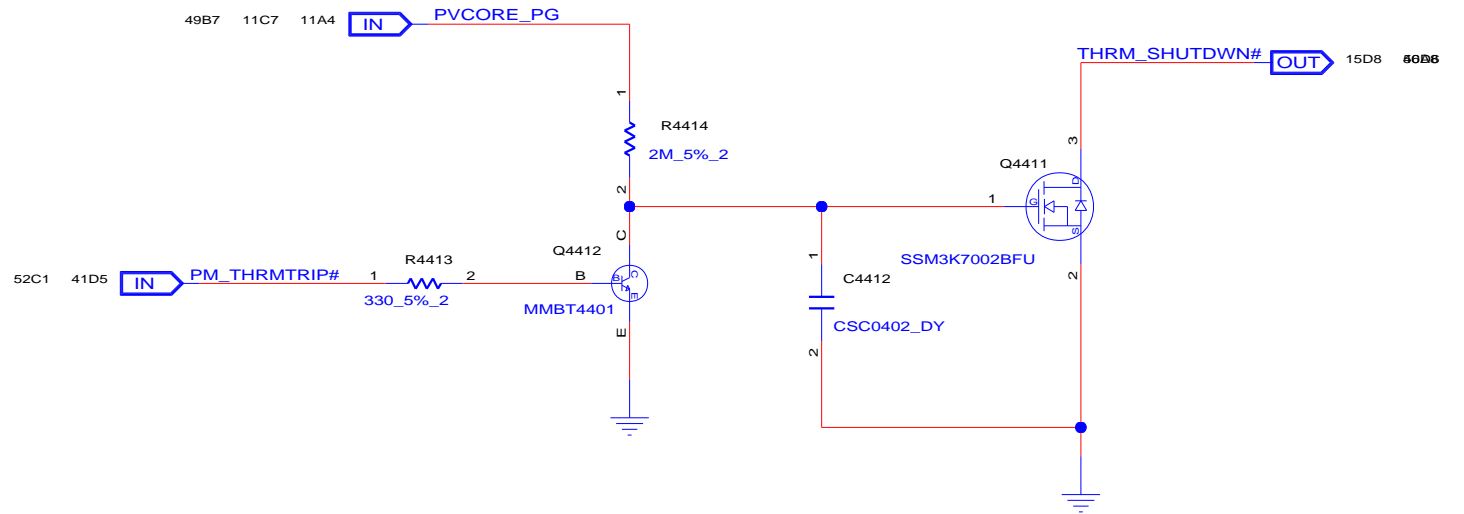
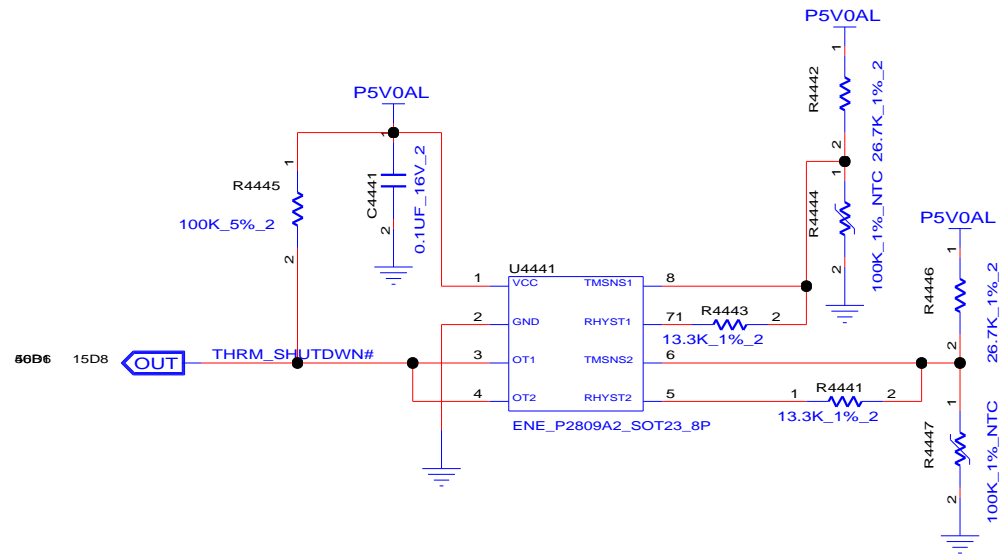




REFERENCE 4300~4349(FAN)  
REFERENCE 4411~4449(THERMAL )



FAN CN



INVENTEC

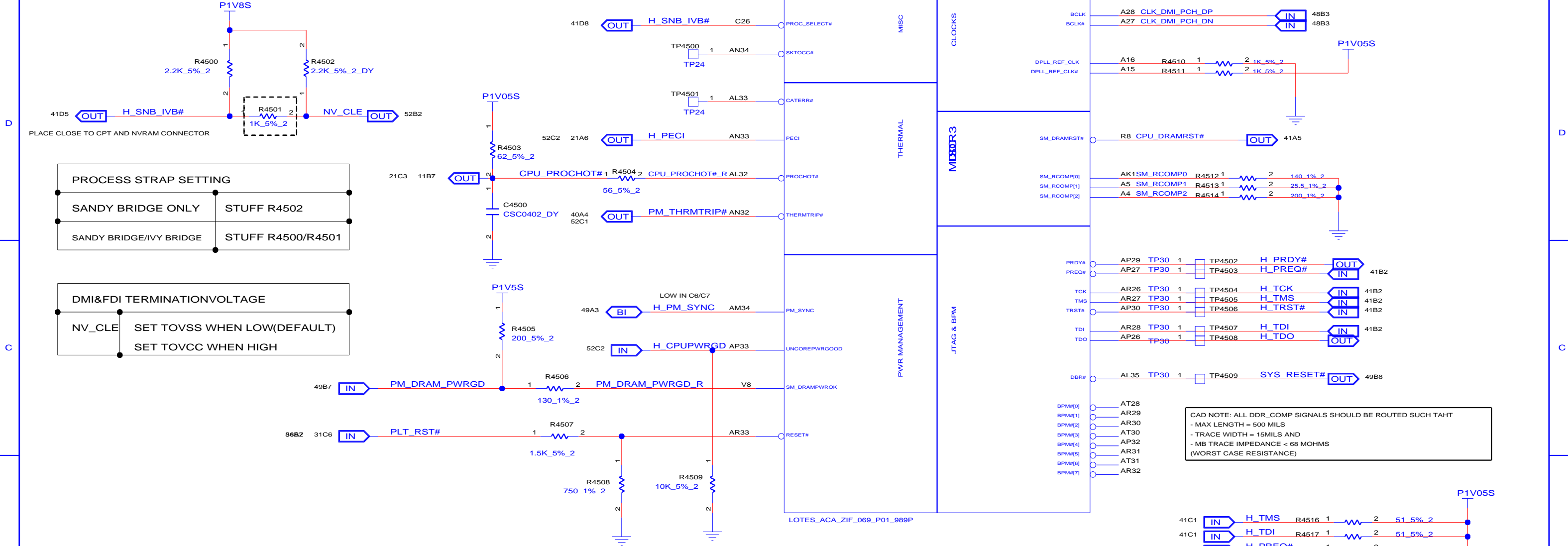
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

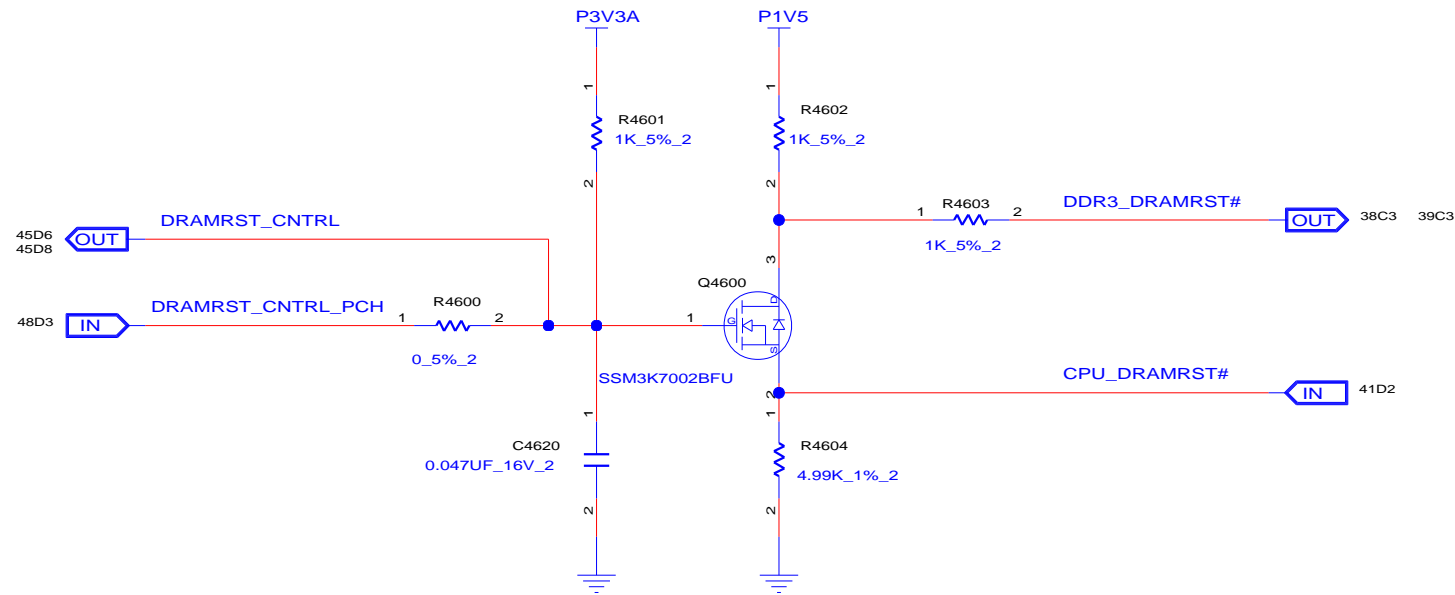
SHEET 40 of 69



REFERENCE 4500~4699(CPU)



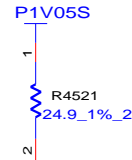
S3 CIRCUIT: DRAM\_RST# TO MEMORY SHOULD BE HIGH DURING S3



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 4500~4699(CPU)



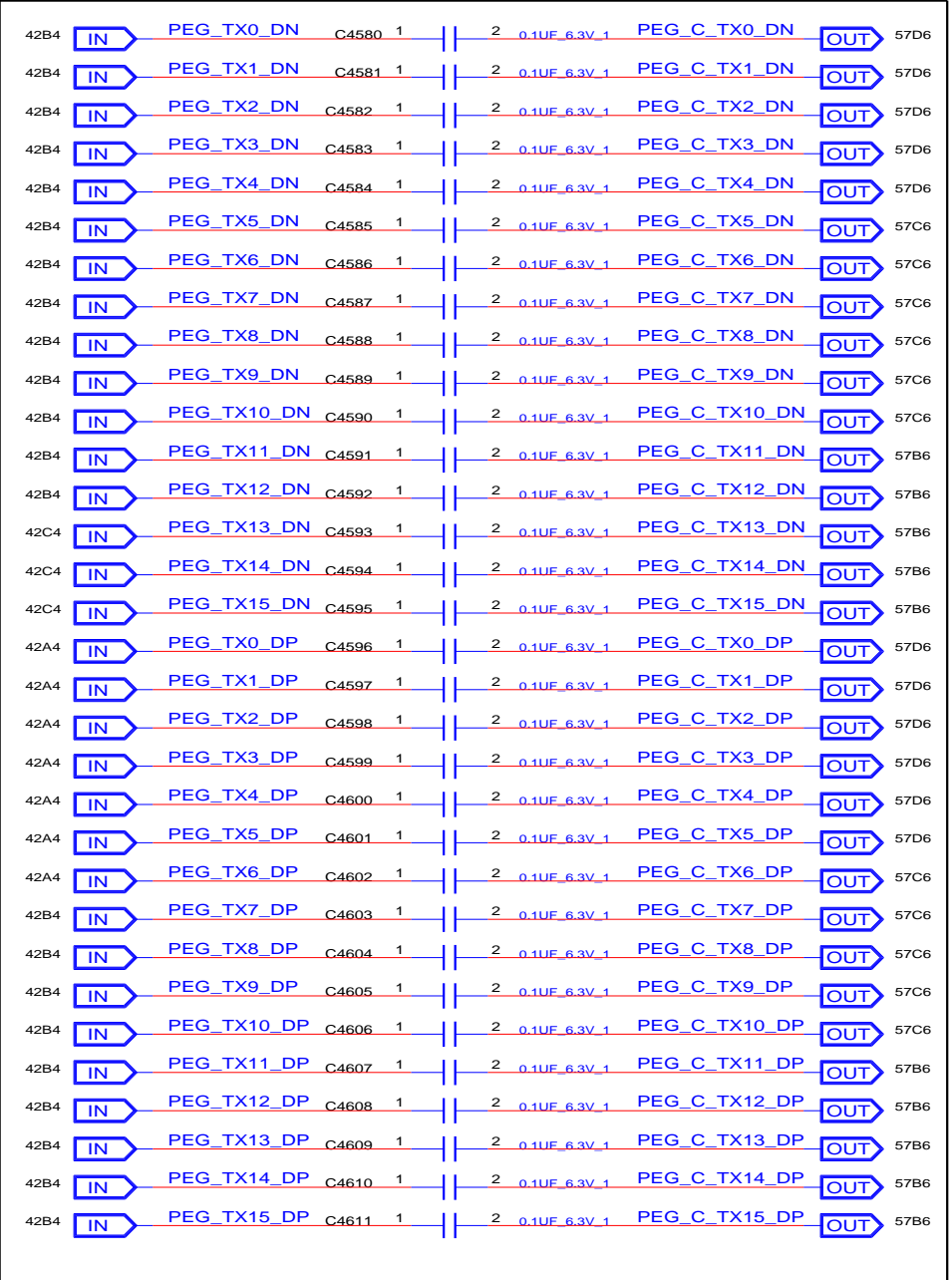
CAD NOTE: DP\_COMPIO AND ICOMPO SIGNALS SHOULD BE SHORTED NEAR BALLS AND ROUTED WITH - TYPICAL IMPEDANCE < 25 MOHMS

CAD NOTE: PEG\_ICOMPI AND RCOMPO SIGNALS SHOULD BE SHORTED AND ROUTED WITH - MAX LENGTH = 500 MILS - TYPICAL IMPEDANCE = 43 MOHMS

PEG\_ICOMPO SIGNALS SHOULD BE ROUTED WITH - MAX LENGTH = 500 MILS - TYPICAL IMPEDANCE = 14.5 MOHMS



CLOSE TO CPU



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 4500~4699(CPU)

SOCKET,CPU,989P,TIN,3.0MM,STR,SMD,TR

D

C

B

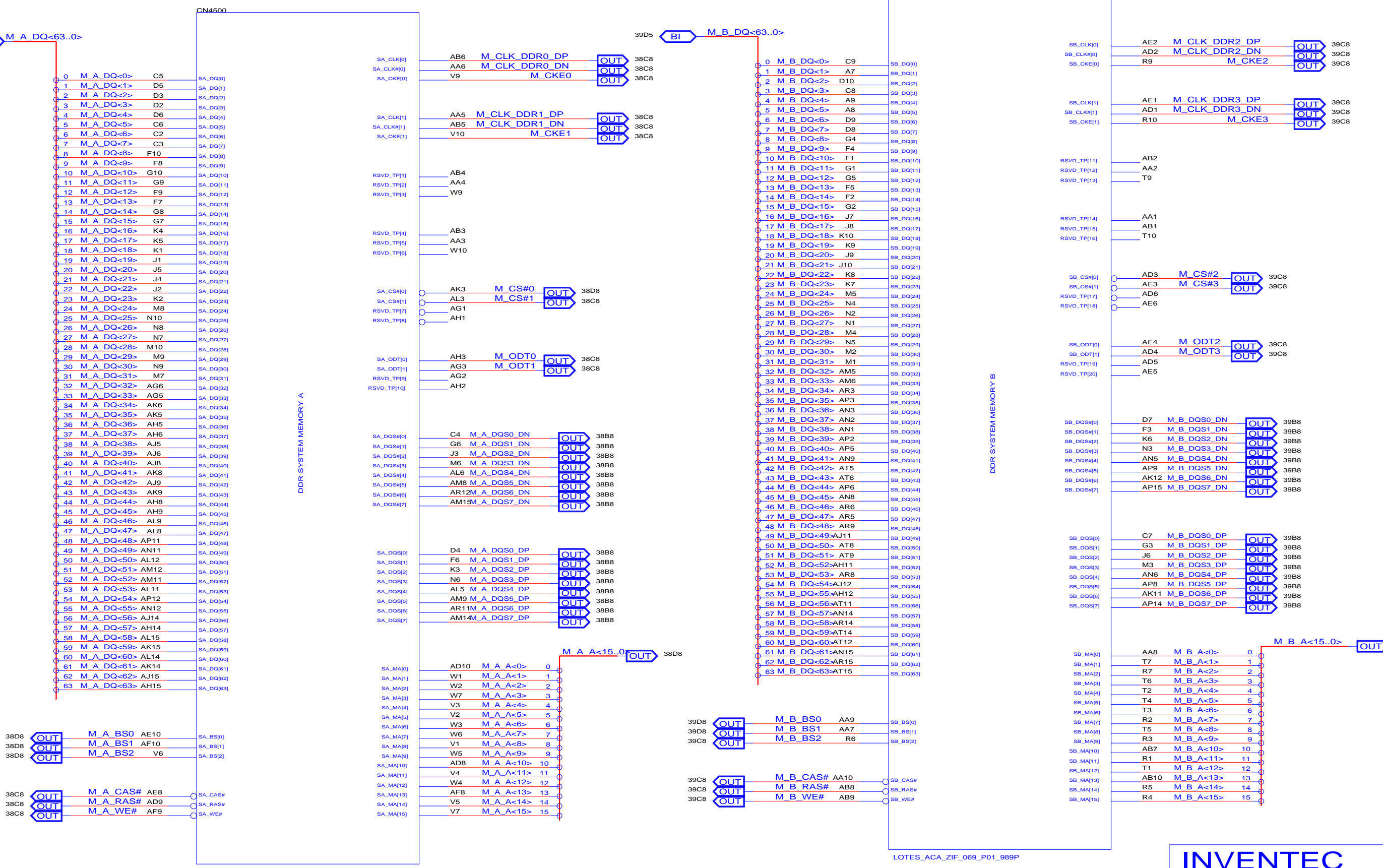
A

D

C

B

A

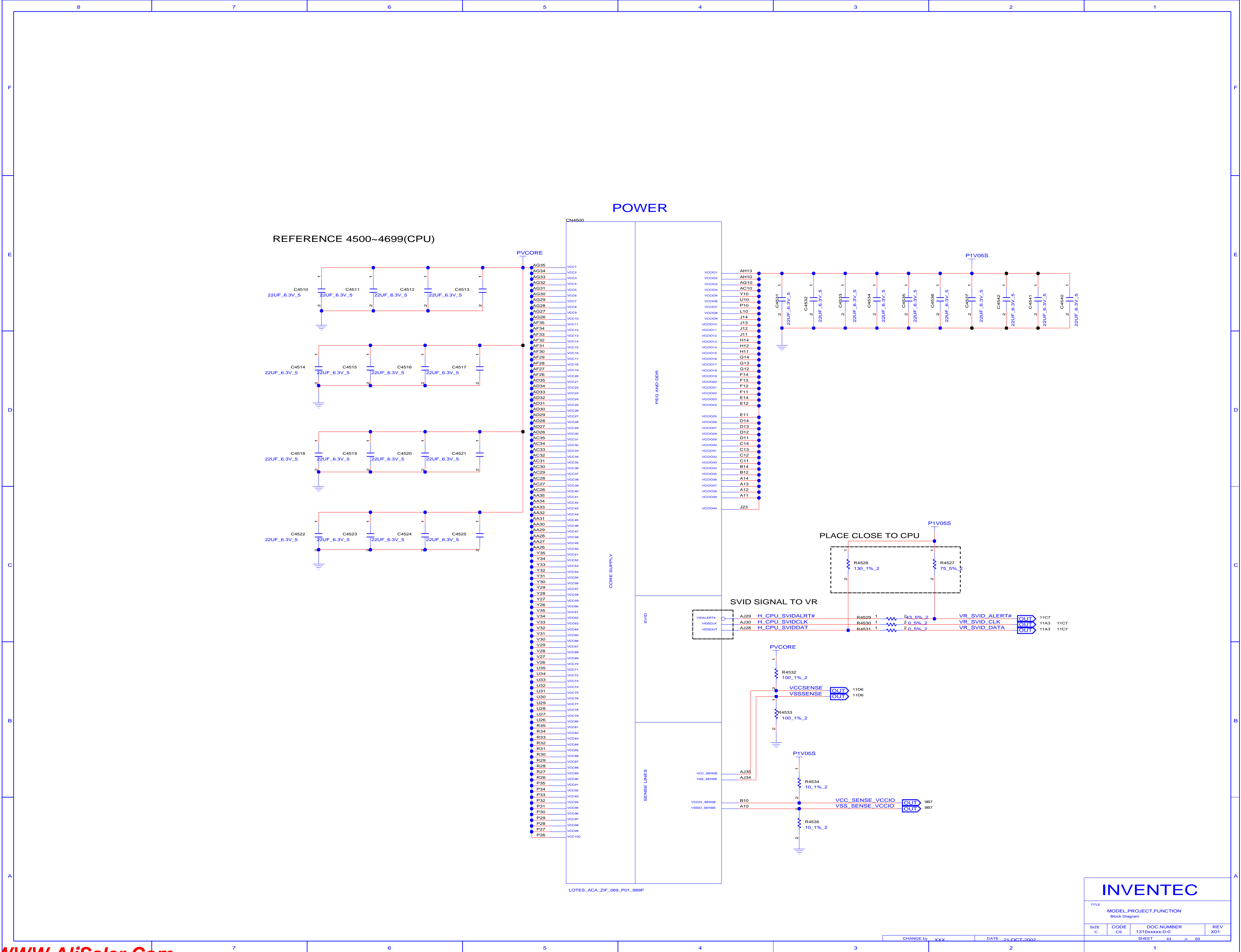


INVENTEC

TITLE MODEL,PROJECT,FUNCTION  
Block Diagram

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 43 of 69



POWER

REFERENCE 4500~4699(CPU)

PVCORE

P1V05S

PLACE CLOSE TO CPU

SVID SIGNAL TO VR

SVID

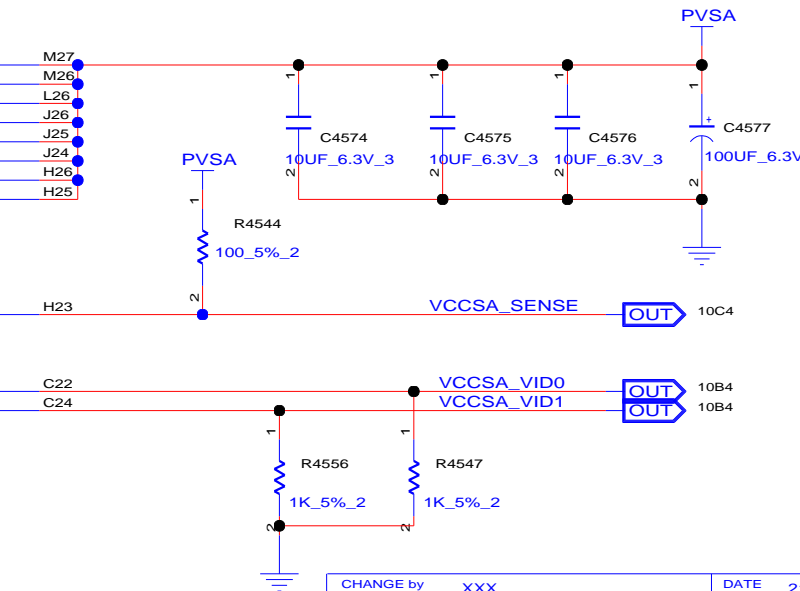
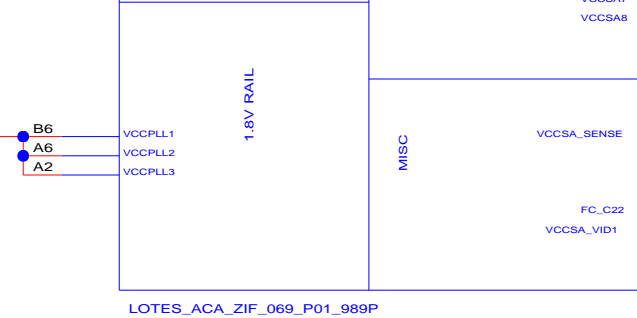
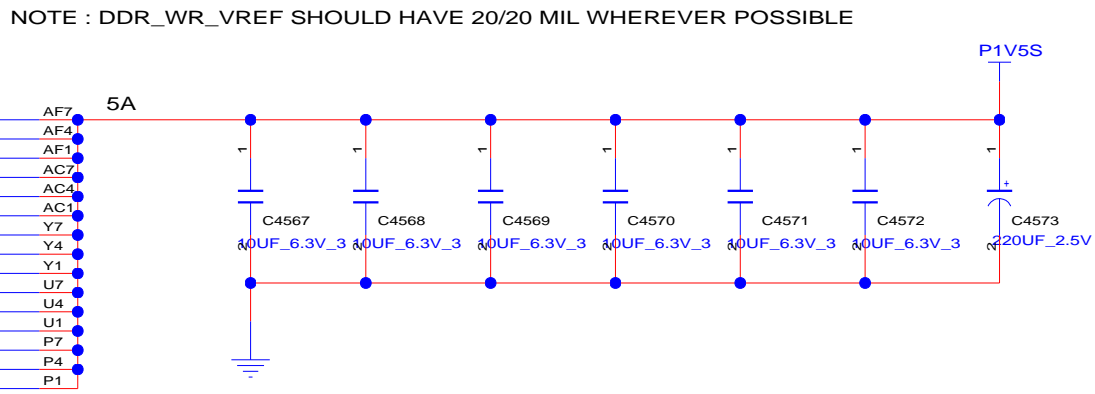
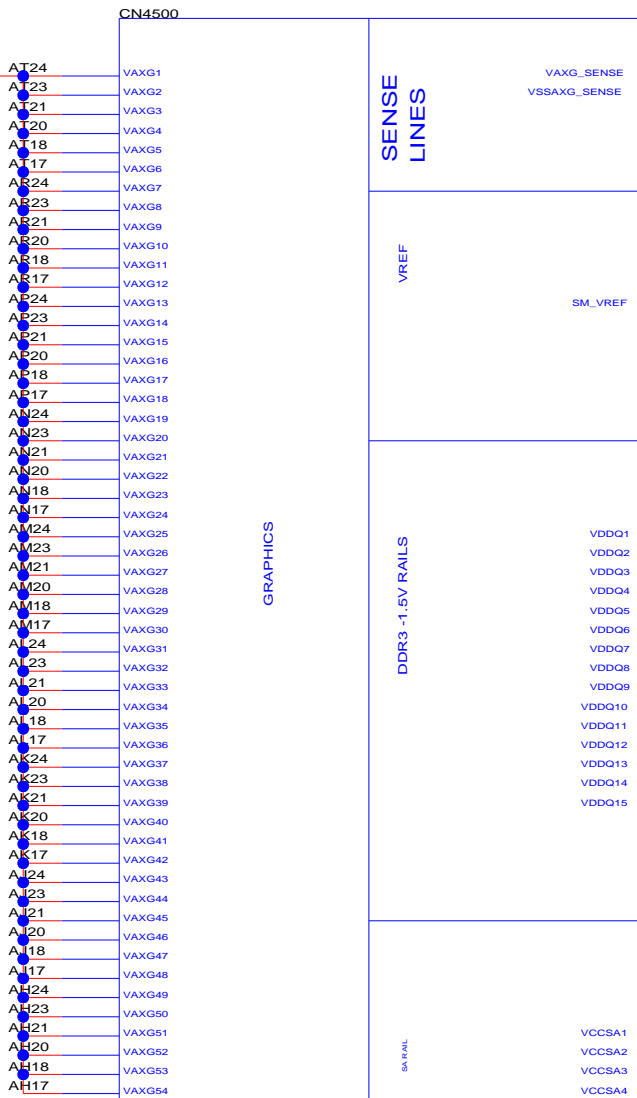
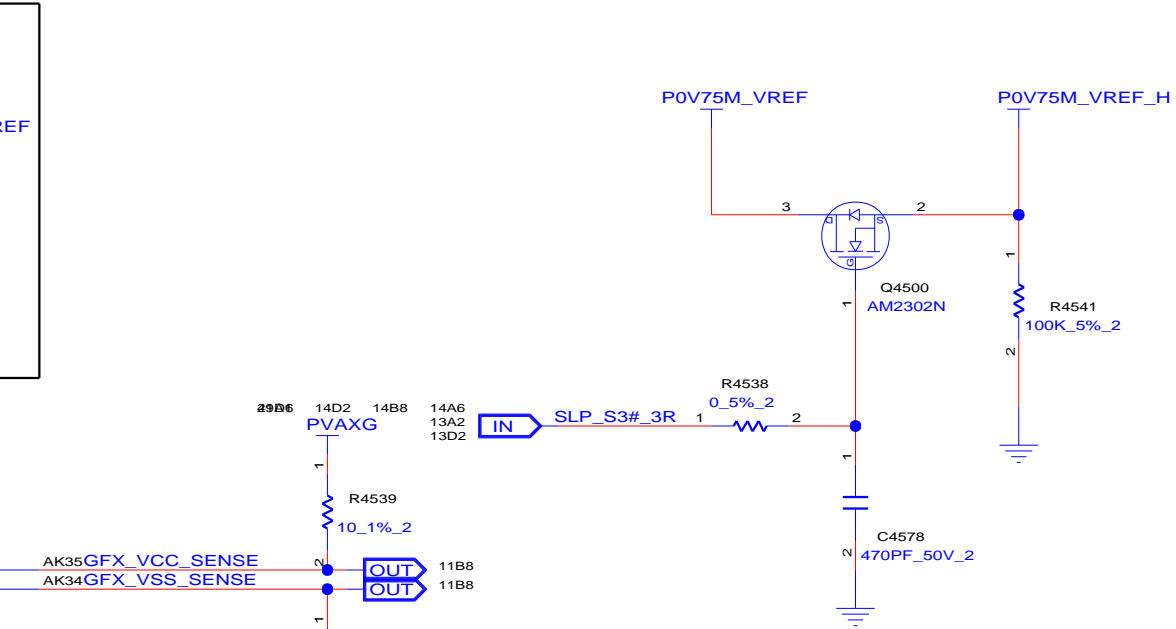
SENSE LINES

LOTES\_ACA\_ZIF\_069\_P01\_989P

INVENTEC

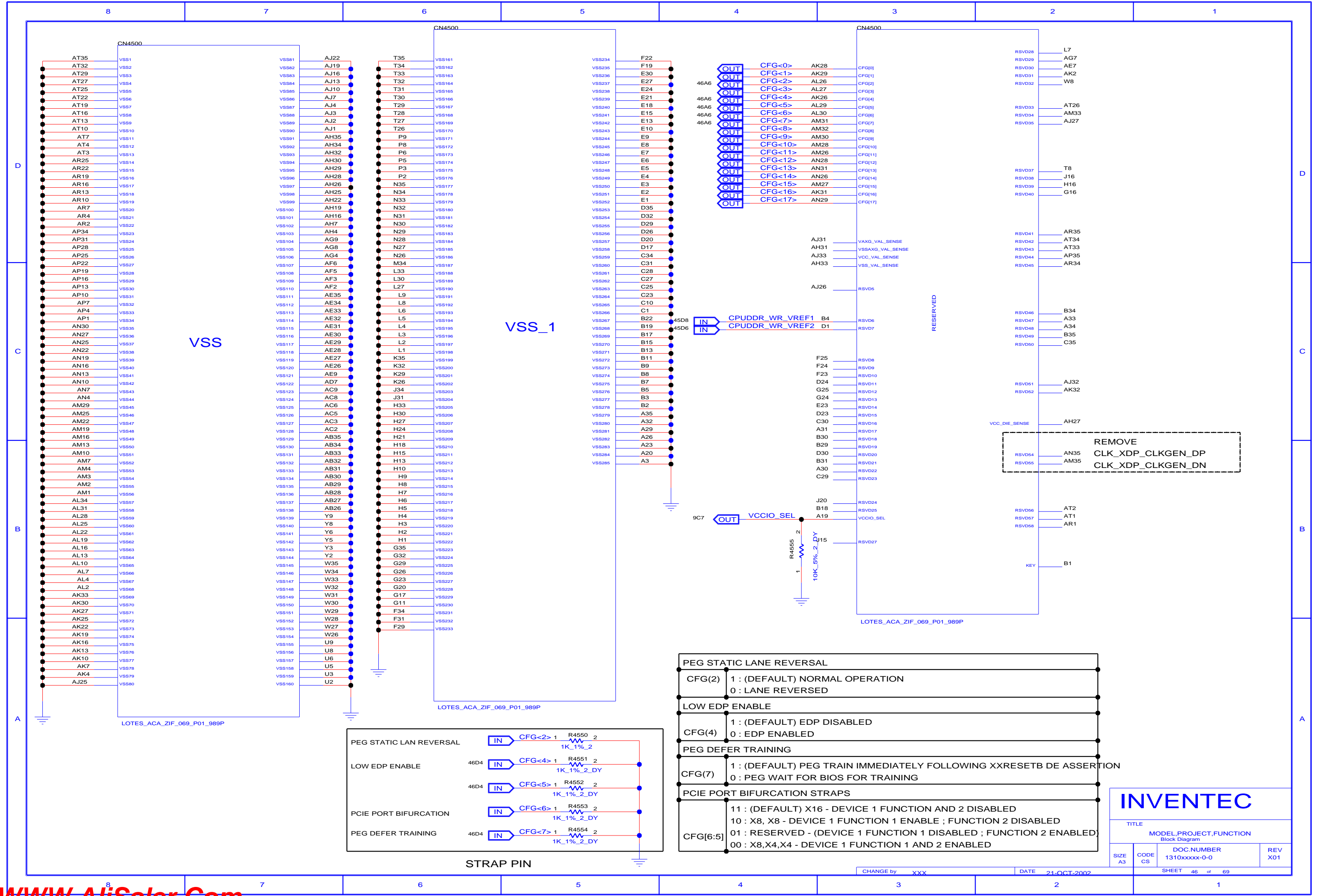
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
C	CS	1310XXXX-0-0	X01
SHEET		44	of 69

CHANGE by XXX DATE 21.OCT.2002

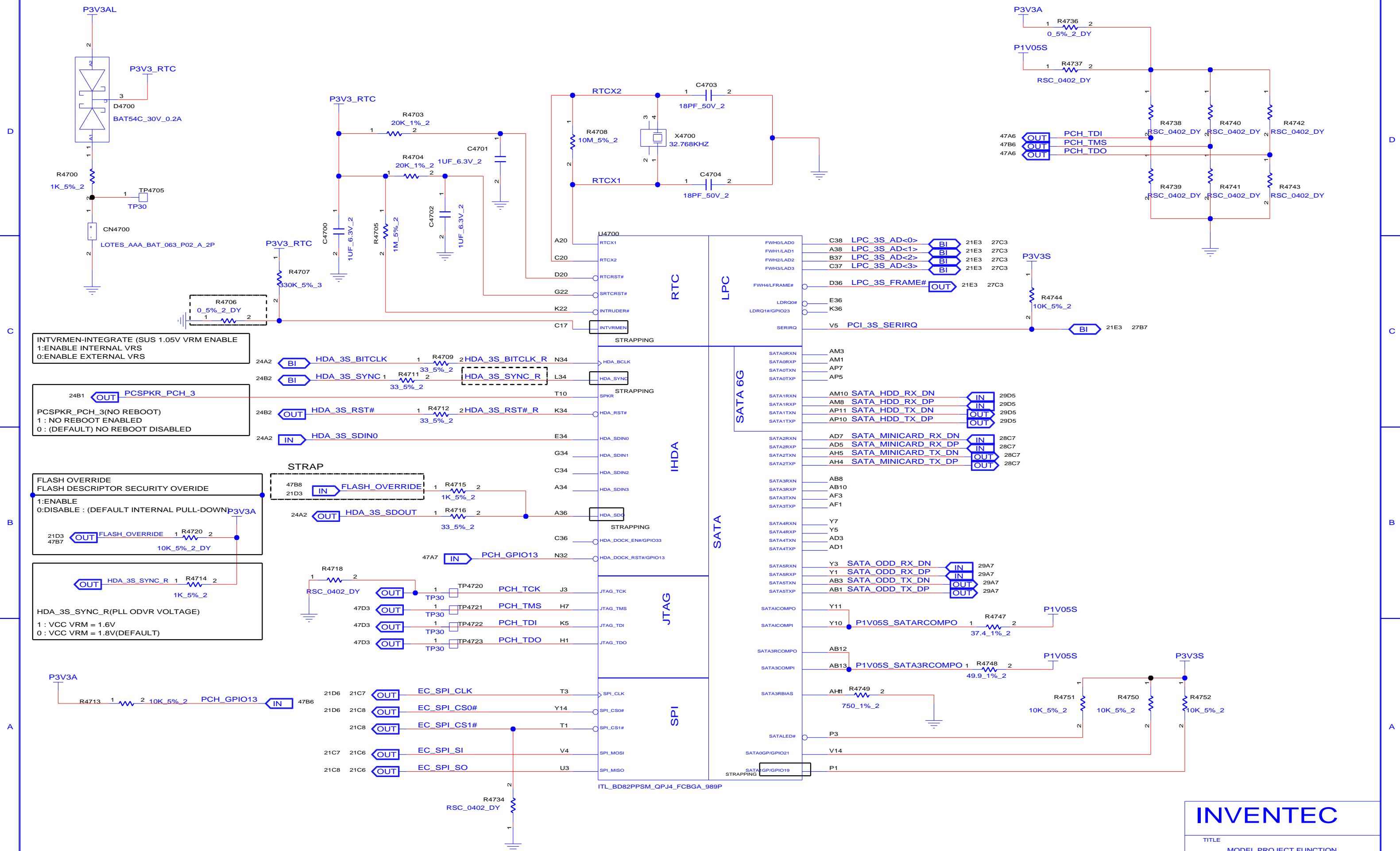


<h1 style="text-align: center;">INVENTEC</h1>			
<p style="text-align: center;">TITLE</p>			
<p style="text-align: center;">MODEL,PROJECT,FUNCTION Block Diagram</p>			
<p style="text-align: center;">SIZE A3</p>	<p style="text-align: center;">CODE CS</p>	<p style="text-align: center;">DOC. NUMBER 1310xxxxx-0-0</p>	<p style="text-align: center;">REV X01</p>
<p style="text-align: center;">SHEET      45      of      69</p>			





REFERENCE 4700~4949(PCH)



# INVENTEC

TITLE	MODEL PROJECT FUNCTION
1. <u>Introduction</u>	
2. <u>Background</u>	
3. <u>Objectives</u>	
4. <u>Methodology</u>	
5. <u>Results</u>	
6. <u>Conclusion</u>	
7. <u>References</u>	
8. <u>Appendix</u>	
9. <u>Summary</u>	
10. <u>Final Report</u>	

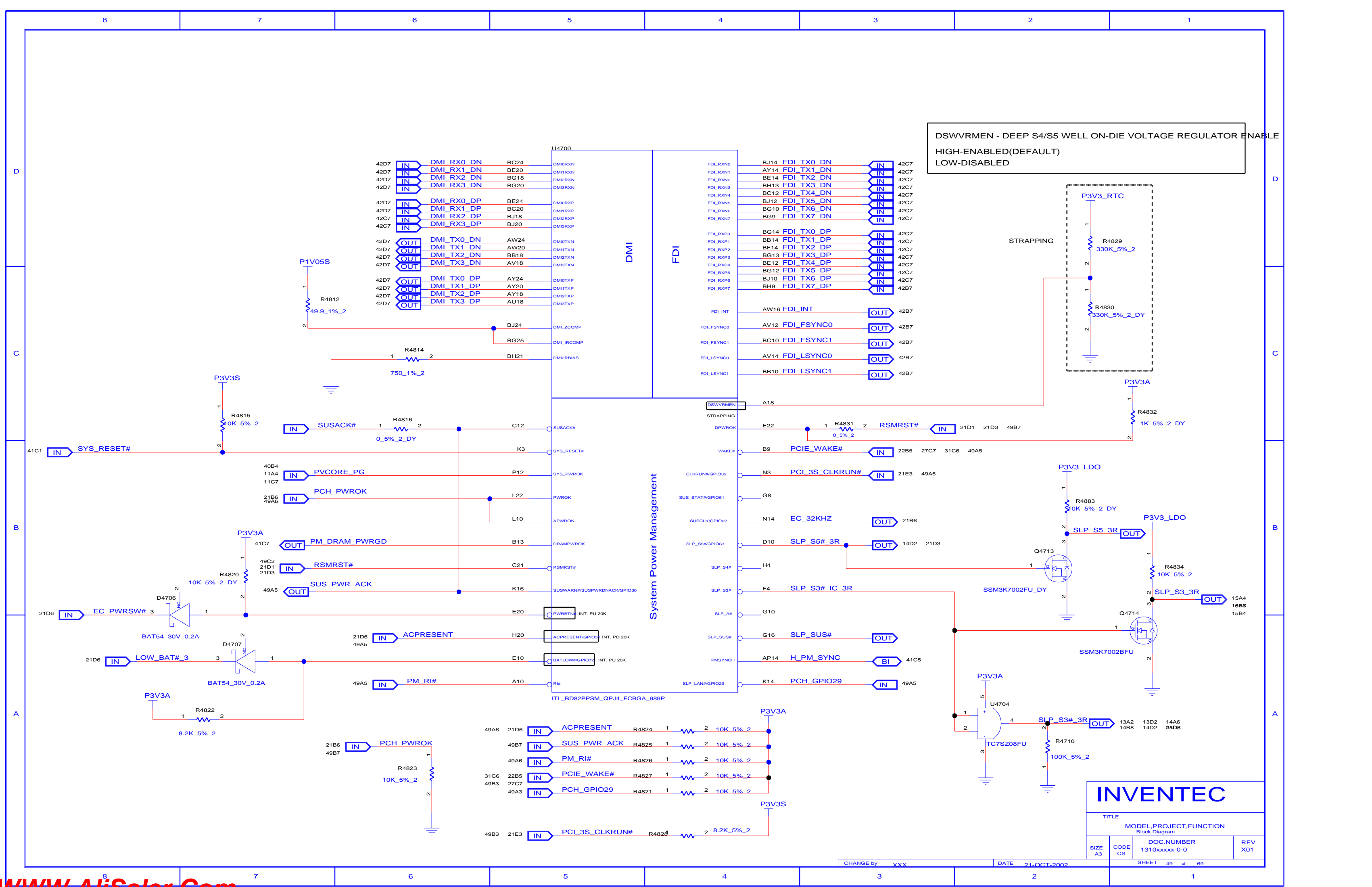
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0
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CHANGE by	XXX	DATE	21-OCT-2002
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SHEET 47 of 69





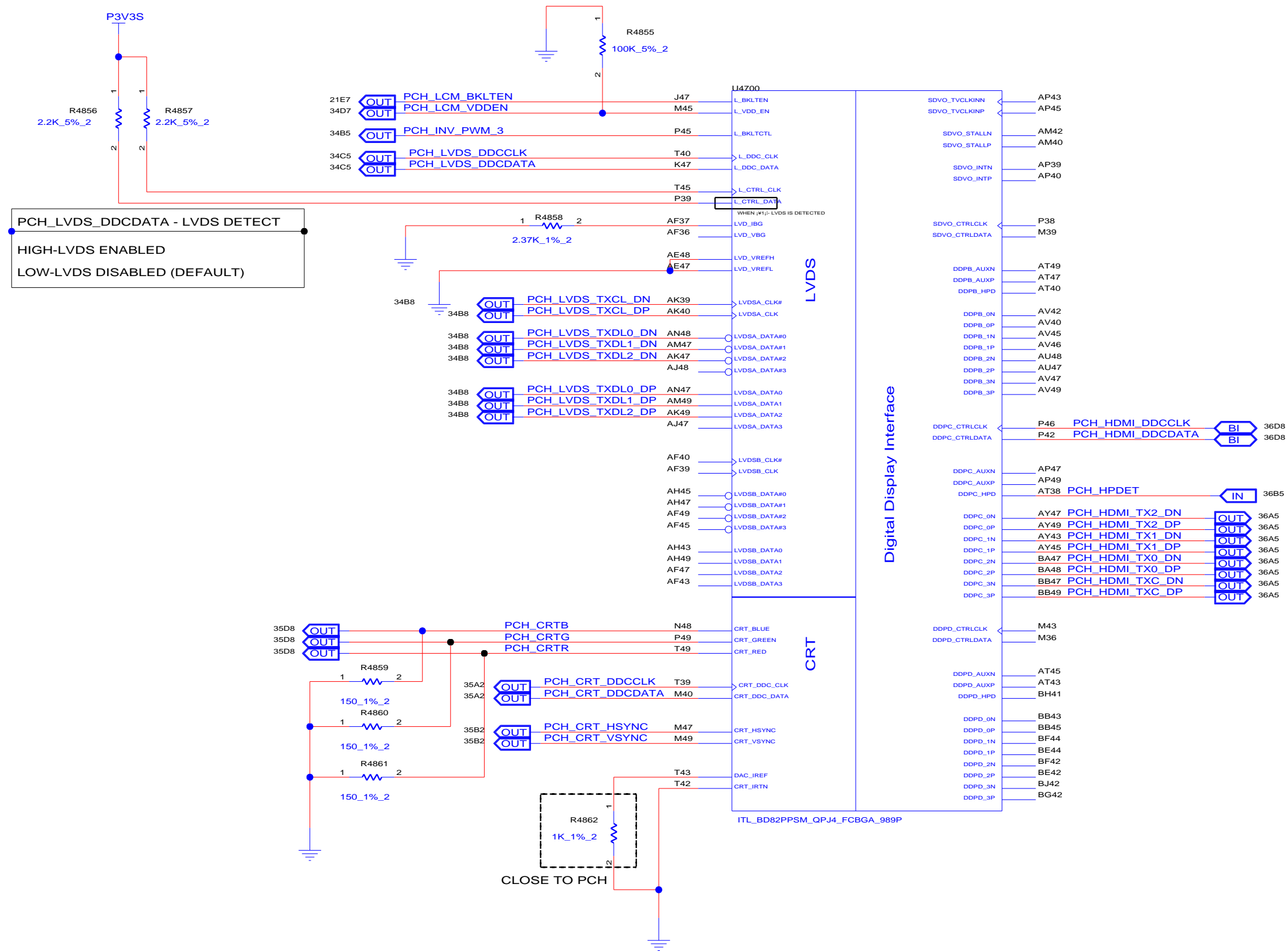


INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 49 of 69

REFERENCE 4700~4949(PCH)



INVENTEC

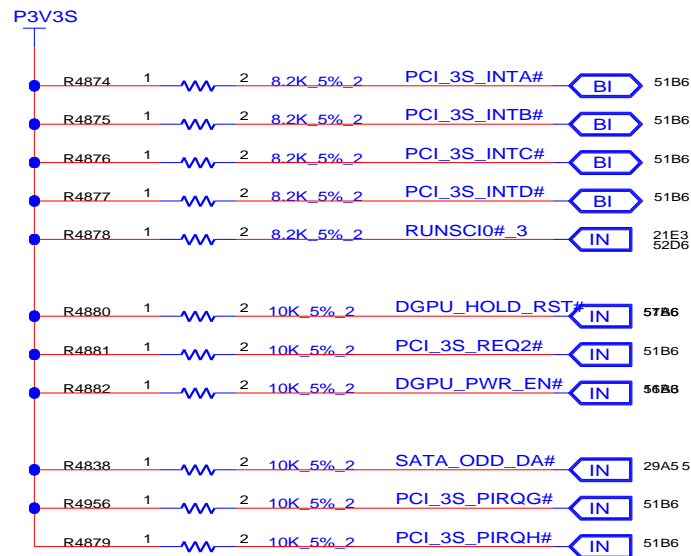
TITLE  
MODEL,PROJECT,FUNCTION  
Block Diagram

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

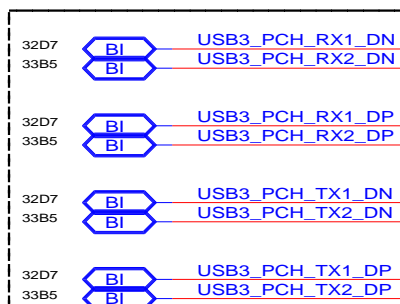
CHANGE by XXX DATE 21-OCT-2002 SHEET 50 of 69

REFERENCE 4700~4949(PCH)

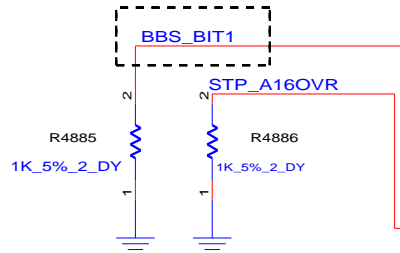
GPIO51 BBS_BIT1	GPIO19 BBS_BIT0	BOOT BIOS DESTINATION
0	1	RESERVED(NAND)
1	0	-----
1	1	SPI (DEFAULT)
0	0	LPC



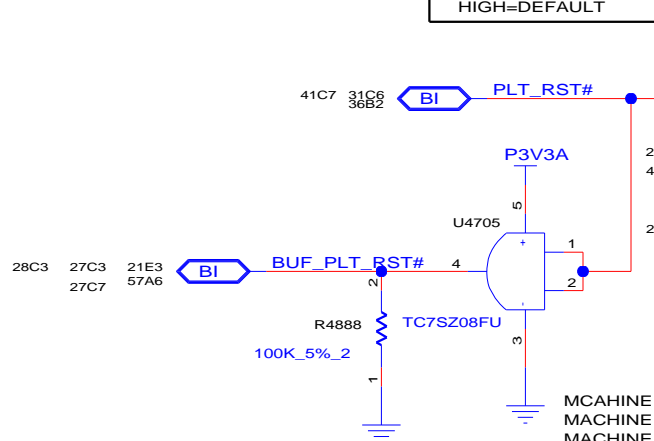
ROUTE WITH 90 OHMS IMPEDANCE  
TOTAL LENGTH NO LONGER THAN 11 INCHES



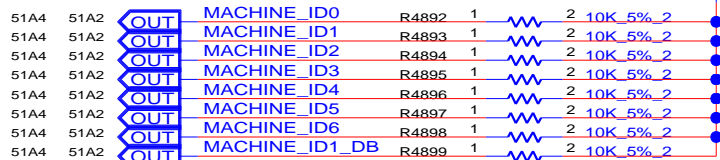
BBS STRAPING



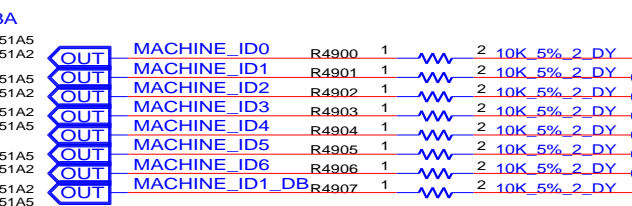
LOW=A16 SWAP OVERRIDE  
STP\_A16OVR  
TOP-BLOCK SWAP OVERRIDE  
HIGH=DEFAULT



MACHINE ID 0: CHIEF RIVER:1 / HURON RIVER 0  
MACHINE ID 1: POWER EXPRESS(YES:1 NO:0)  
MACHINE ID 2: UMA:1 / DISCRETE:0  
MACHINE ID 3: MAINSTREAM:1 / ENTRY:0  
MACHINE ID 4: HDMI:1 / NO HDMI:0  
MACHINE ID 5: SLEEP&CHARGE&DOLBY:YES1 / NO 0  
MACHINE ID 6: 35W CPU:1 / 17W CPU:0



NOTE:10K\_5%(60130B1030ZT)



NOTE:10K\_5%(60130B1030ZT)

INVENTEC

TITLE

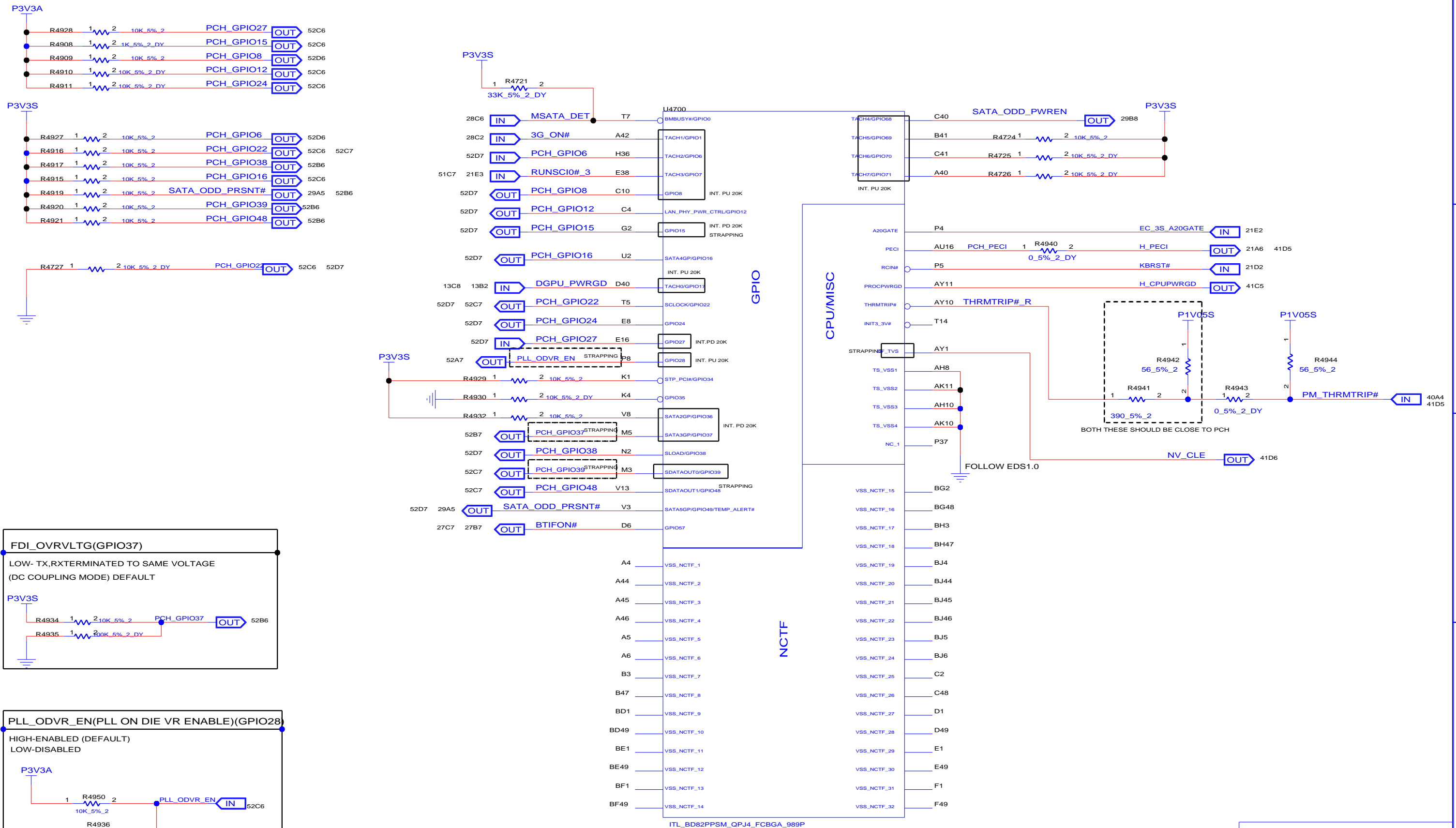
MODEL,PROJECT,FUNCTION  
Block Diagram

SIZE A3 CODE CS DOC.NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX

DATE 21-OCT-2002

SHEET 51 of 69



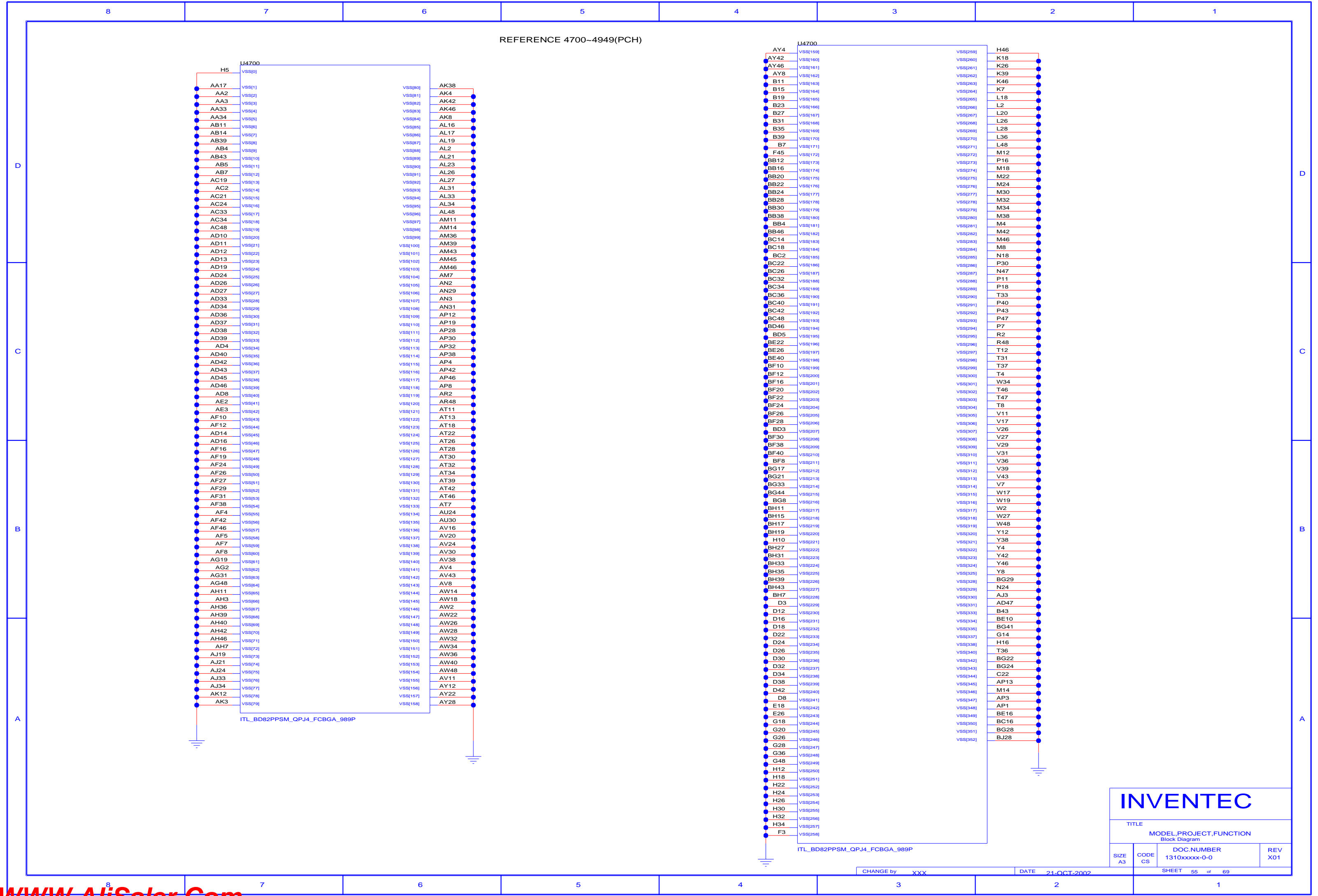
# INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET 52 of 69			







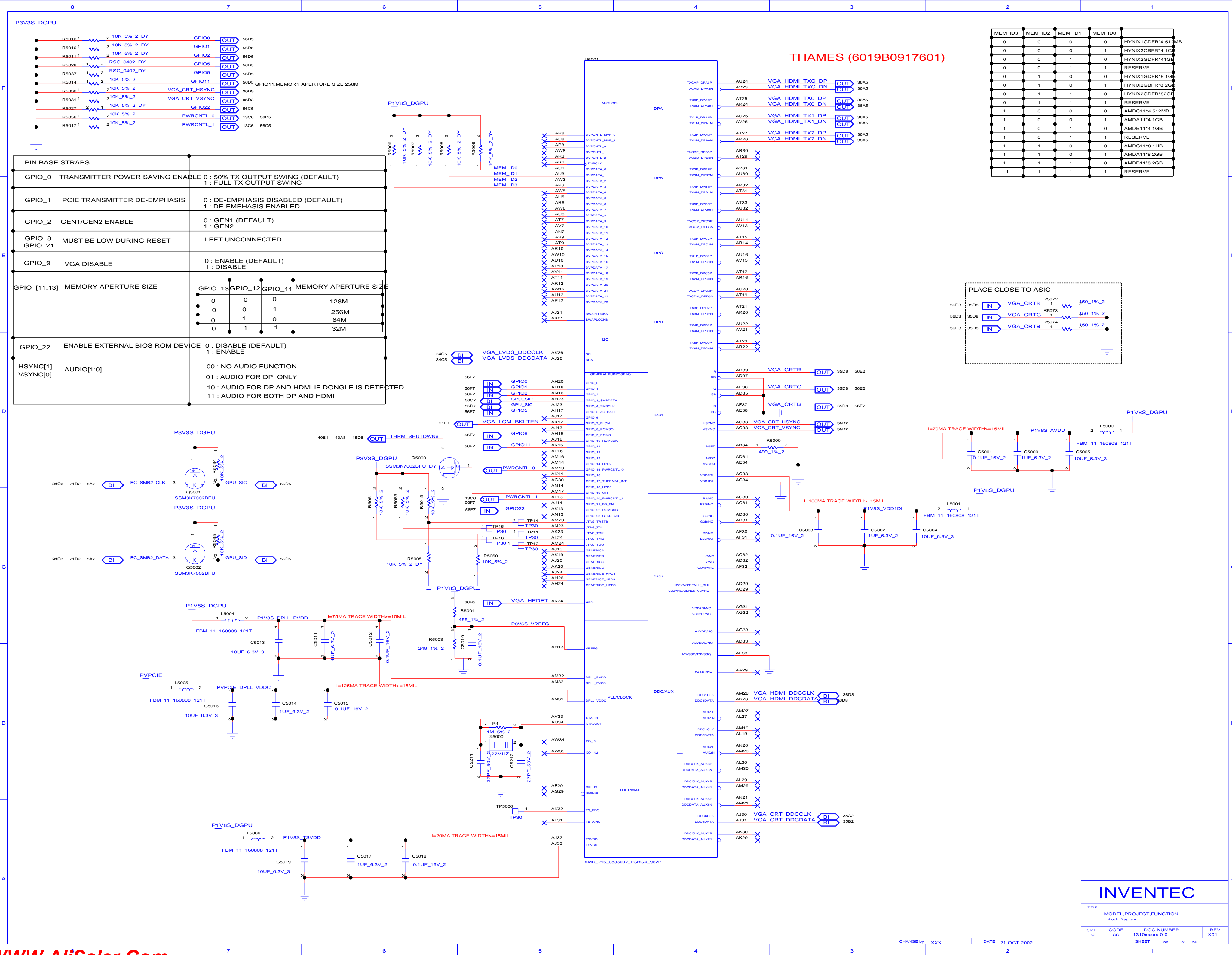


INVENTEC

TITLE  
MODEL,PROJECT,FUNCTION  
Block Diagram

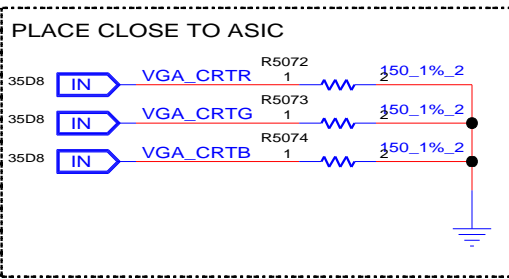
SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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CHANGE by XXX DATE 21-OCT-2002 SHEET 55 of 69



PIN BASE STRAPS		
GPIO_0	TRANSMITTER POWER SAVING ENABLE	0 : 50% TX OUTPUT SWING (DEFAULT) 1 : FULL TX OUTPUT SWING
GPIO_1	PCIE TRANSMITTER DE-EMPHASIS	0 : DE-EMPHASIS DISABLED (DEFAULT) 1 : DE-EMPHASIS ENABLED
GPIO_2	GEN1/GEN2 ENABLE	0 : GEN1 (DEFAULT) 1 : GEN2
GPIO_8 GPIO_21	MUST BE LOW DURING RESET	LEFT UNCONNECTED
GPIO_9	VGA DISABLE	0 : ENABLE (DEFAULT) 1 : DISABLE
GPIO_[11:13]	MEMORY APERTURE SIZE	GPIO_13GPIO_12GPIO_11MEMORY APERTURE SIZE
		000128M
		001256M
		01064M
		01132M
GPIO_22	ENABLE EXTERNAL BIOS ROM DEVICE	0 : DISABLE (DEFAULT) 1 : ENABLE
HSYNC[1] VSYNC[0]	AUDIO[1:0]	00 : NO AUDIO FUNCTION 01 : AUDIO FOR DP ONLY 10 : AUDIO FOR DP AND HDMI IF DONGLE IS DETECTED 11 : AUDIO FOR BOTH DP AND HDMI

MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	
0	0	0	0	HYNIX1GDFR*4 512MB
0	0	0	1	HYNIX2GBFR*4 1GB
0	0	1	0	HYNIX2GBFR*4 1GB
0	0	1	1	RESERVE
0	1	0	0	HYNIX1GDFR*8 1GB
0	1	0	1	HYNIX2GBFR*8 2GB
0	1	1	0	HYNIX2GBFR*8 2GB
0	1	1	1	RESERVE
1	0	0	0	AMDC11*4 512MB
1	0	0	1	AMDA11*4 1GB
1	0	1	0	AMDB11*4 1GB
1	0	1	1	RESERVE
1	1	0	0	AMDC11*8 1HB
1	1	0	1	AMDA11*8 2GB
1	1	1	0	AMDB11*8 2GB
1	1	1	1	RESERVE



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

Block Diagram

SIZE C

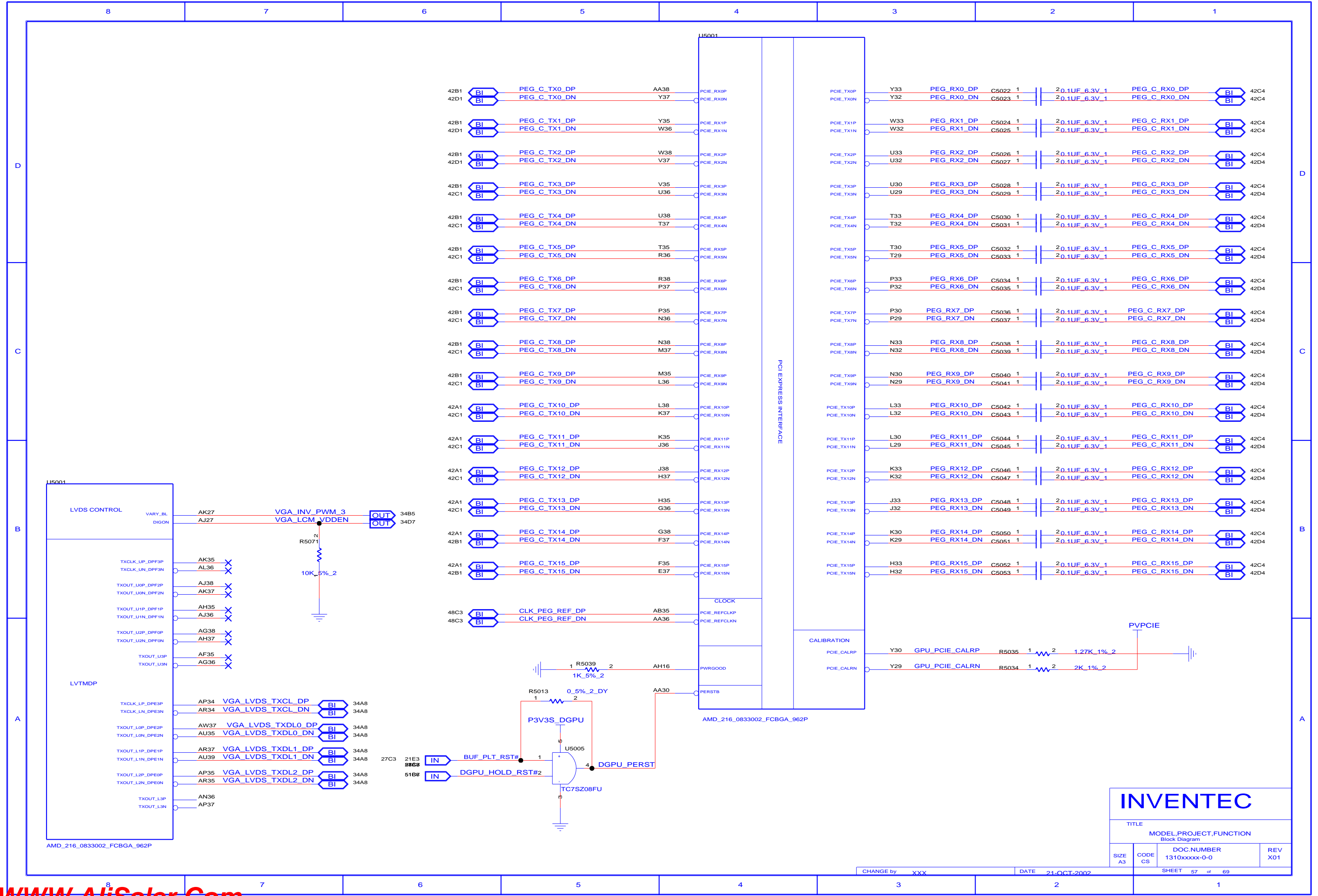
CODE CS

DOC. NUMBER 1310xxxx-0-0

REV X01

SHEET 56 of 69

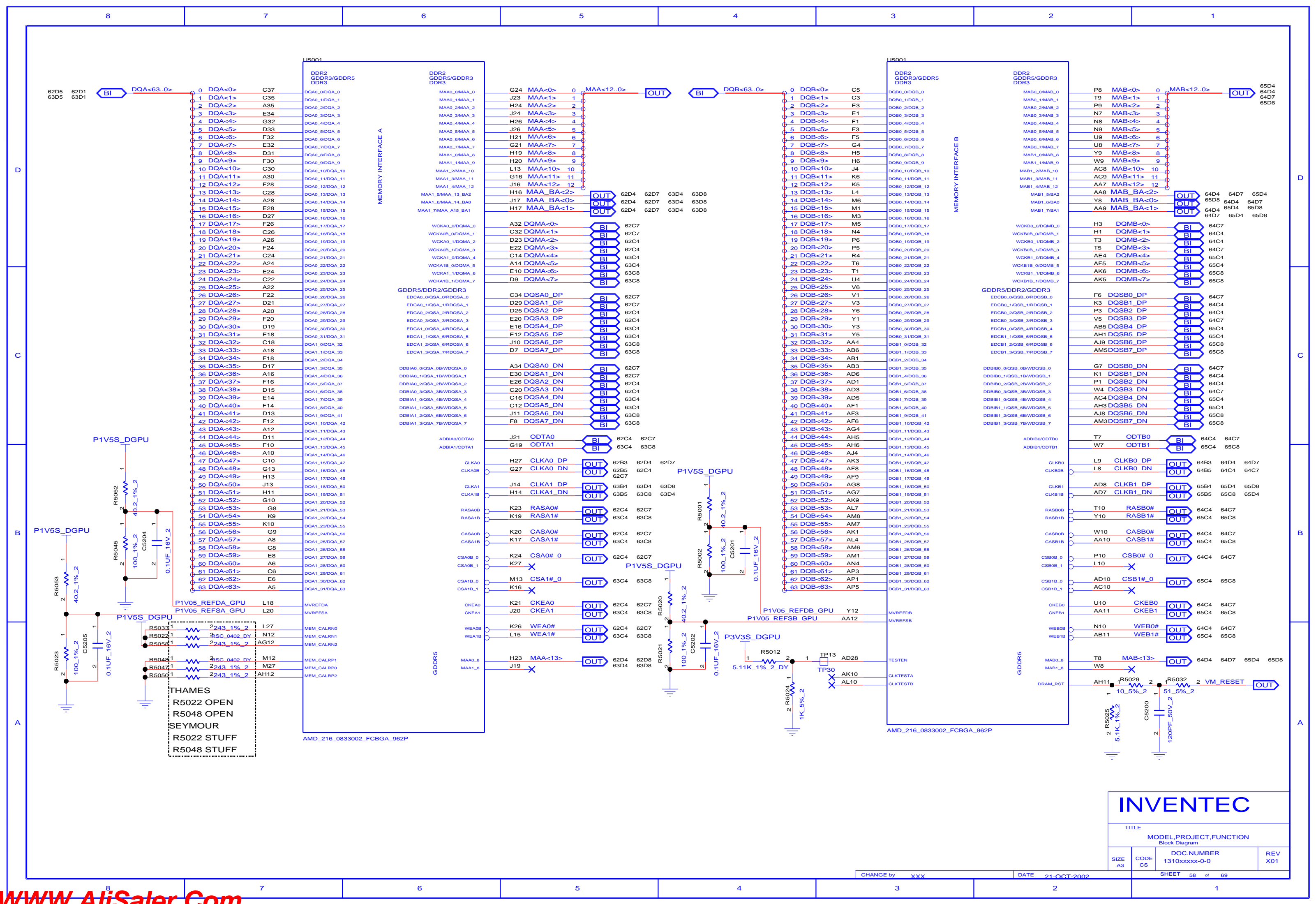




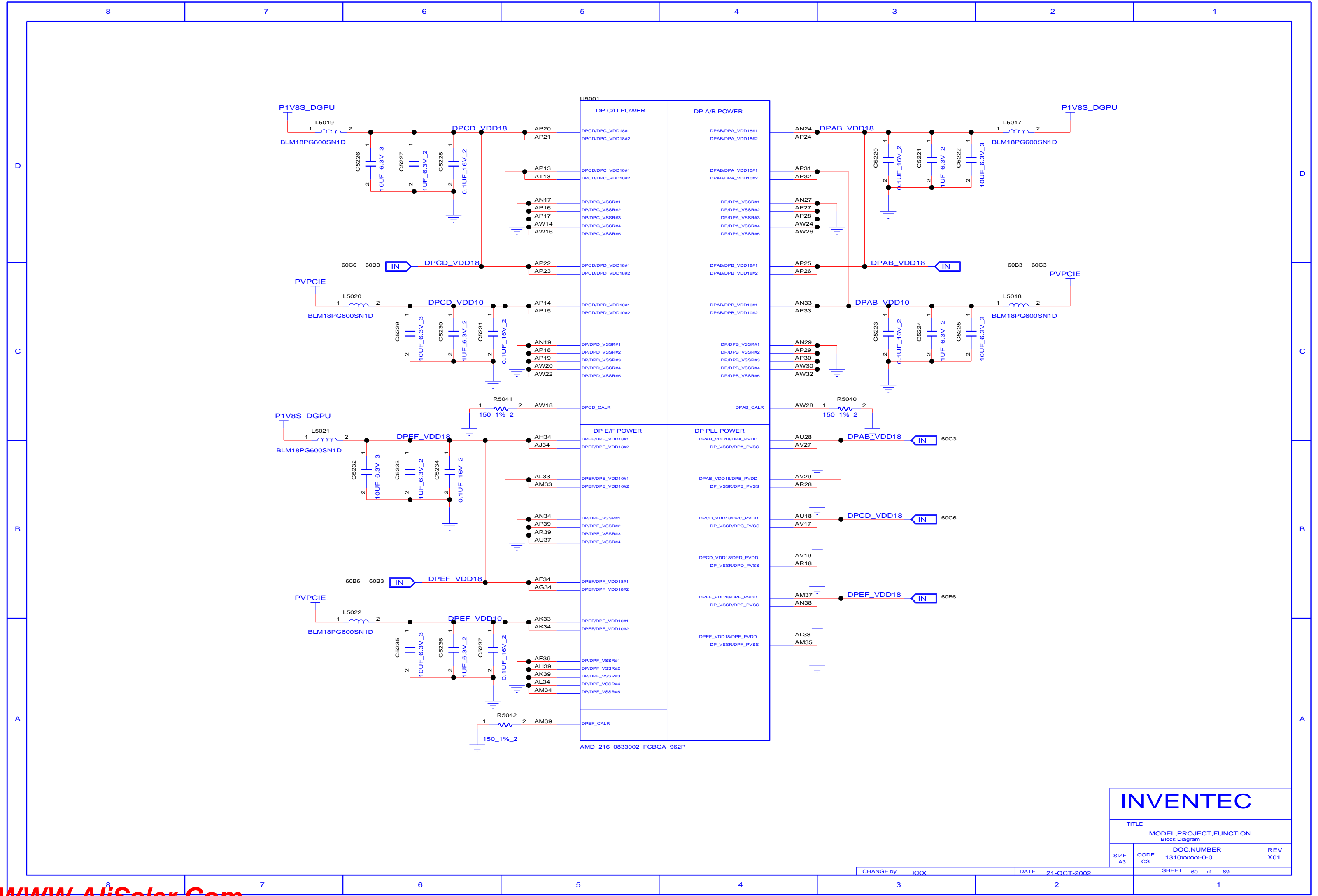
INVENTEC

TITLE MODEL,PROJECT,FUNCTION  
Block Diagram

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01





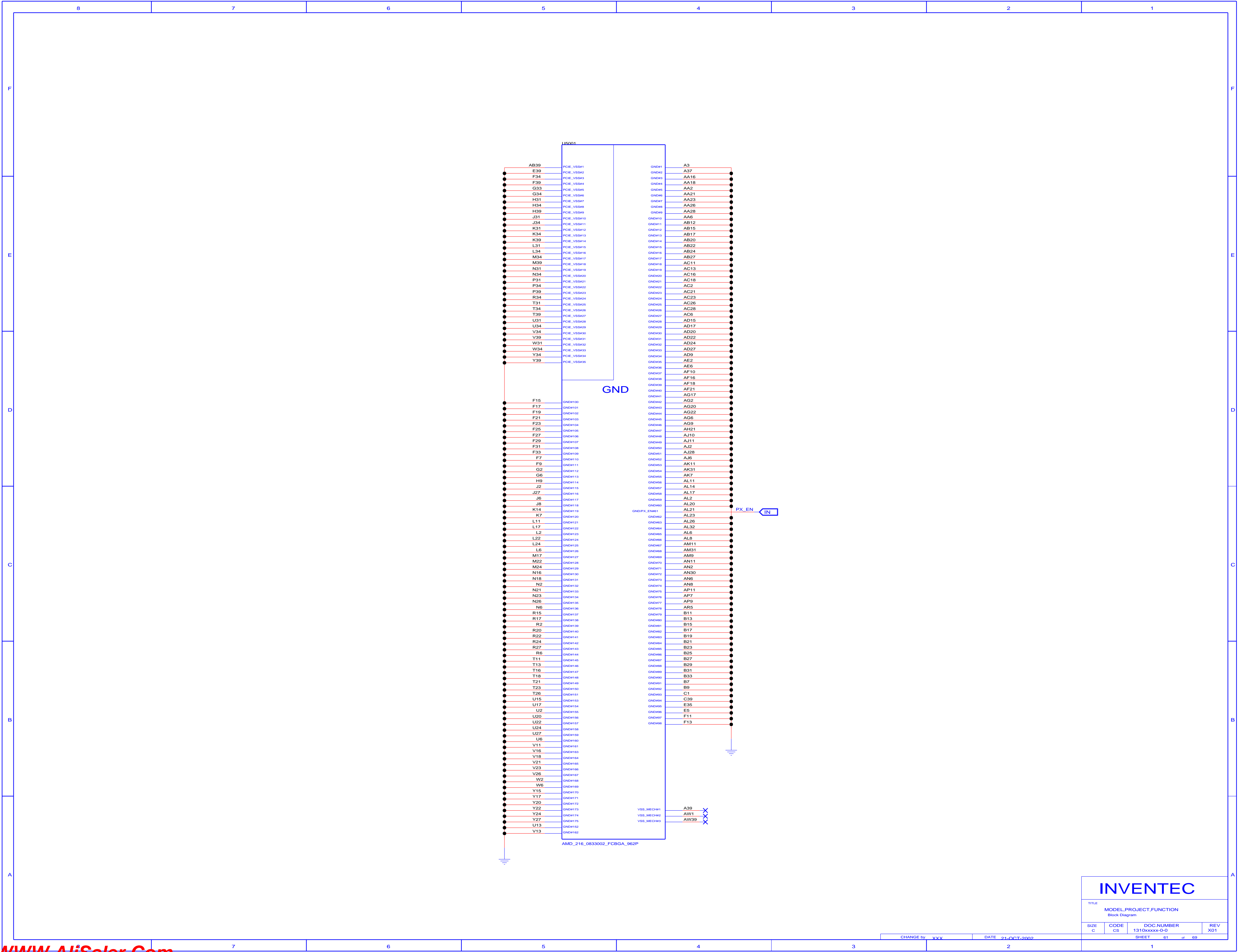


INVENTEC

TITLE  
MODEL,PROJECT,FUNCTION  
Block Diagram

SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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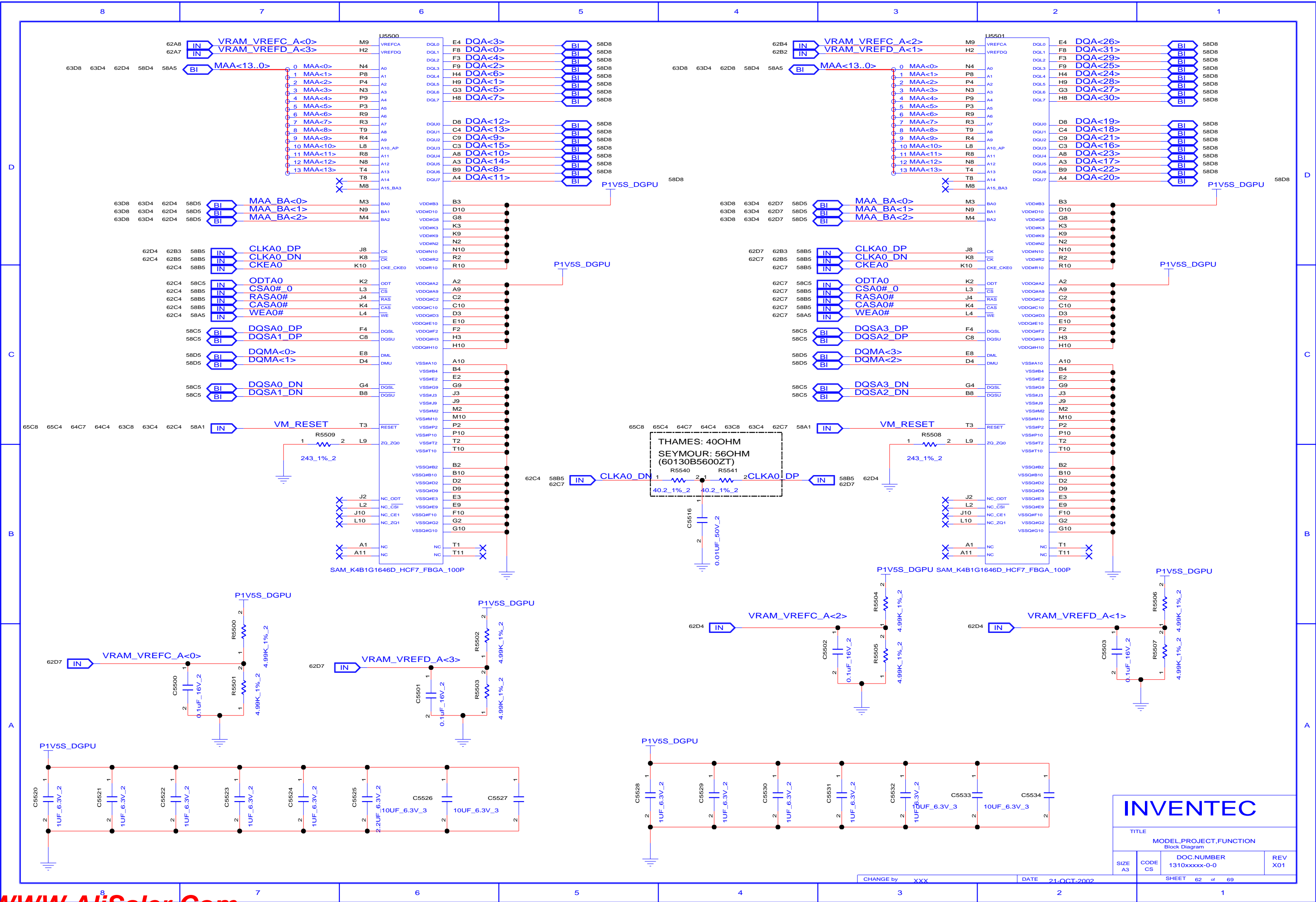
CHANGE by XXX DATE 21-OCT-2002 SHEET 60 of 69



INVENTEC				
TITLE				
MODEL,PROJECT,FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER	REV	
C	CS	1310xxxxx-0-0	X01	
SHEET		61	of	69

CHANGE by XXX DATE 21.OCT.2002



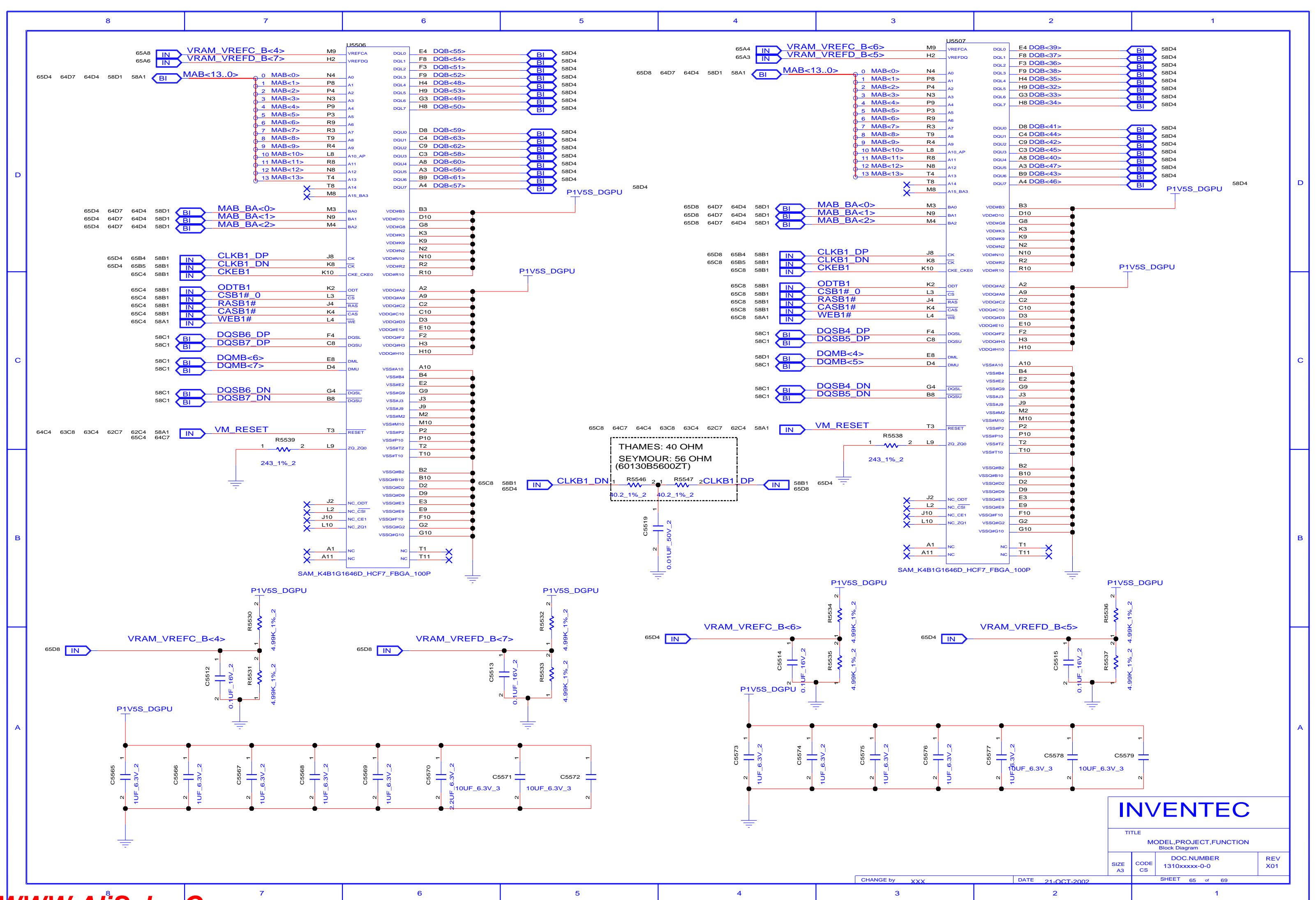


INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

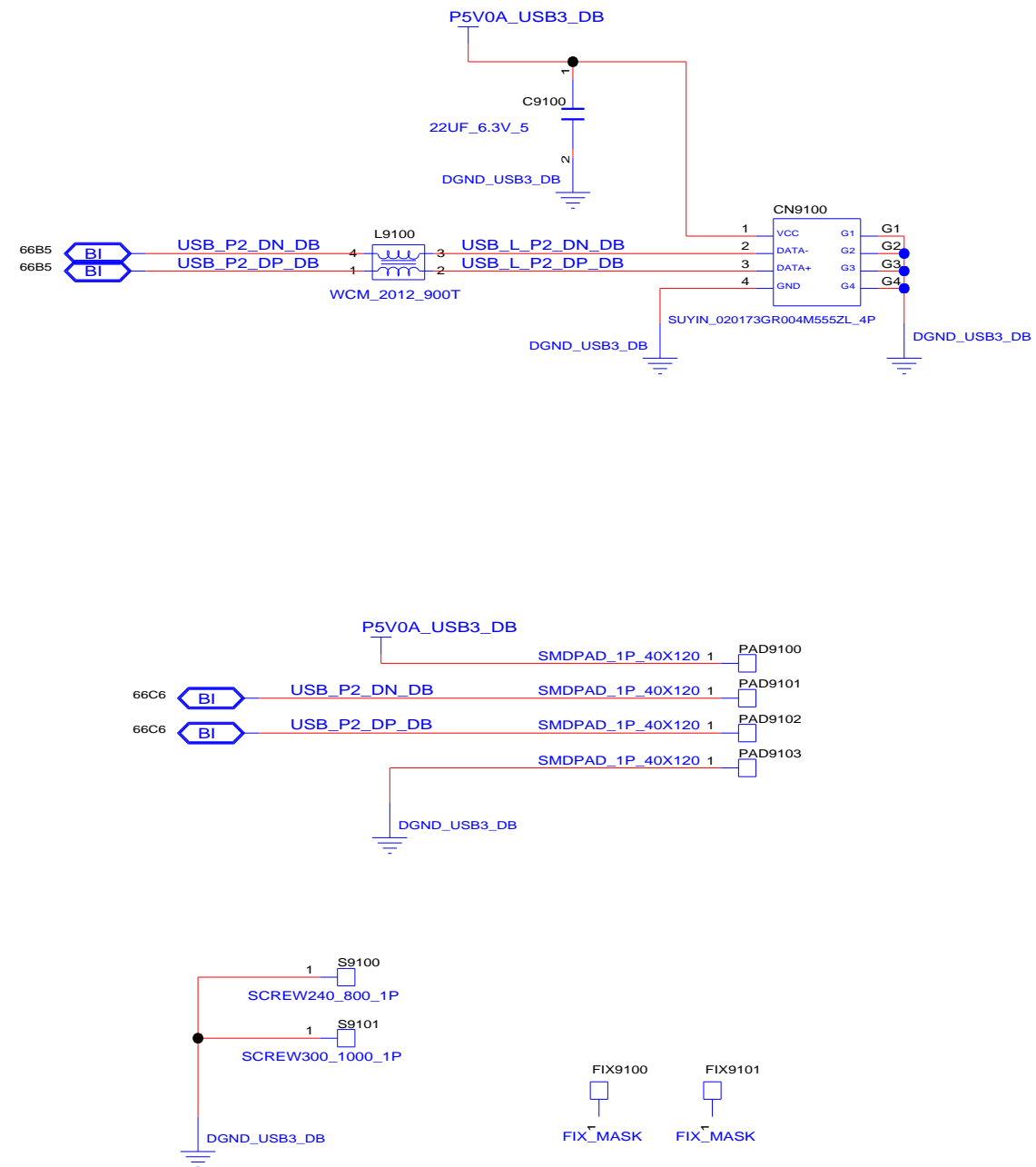








# USB BOARD



INVENTEC

TITLE  
MODEL,PROJECT,FUNCTION  
Block Diagram

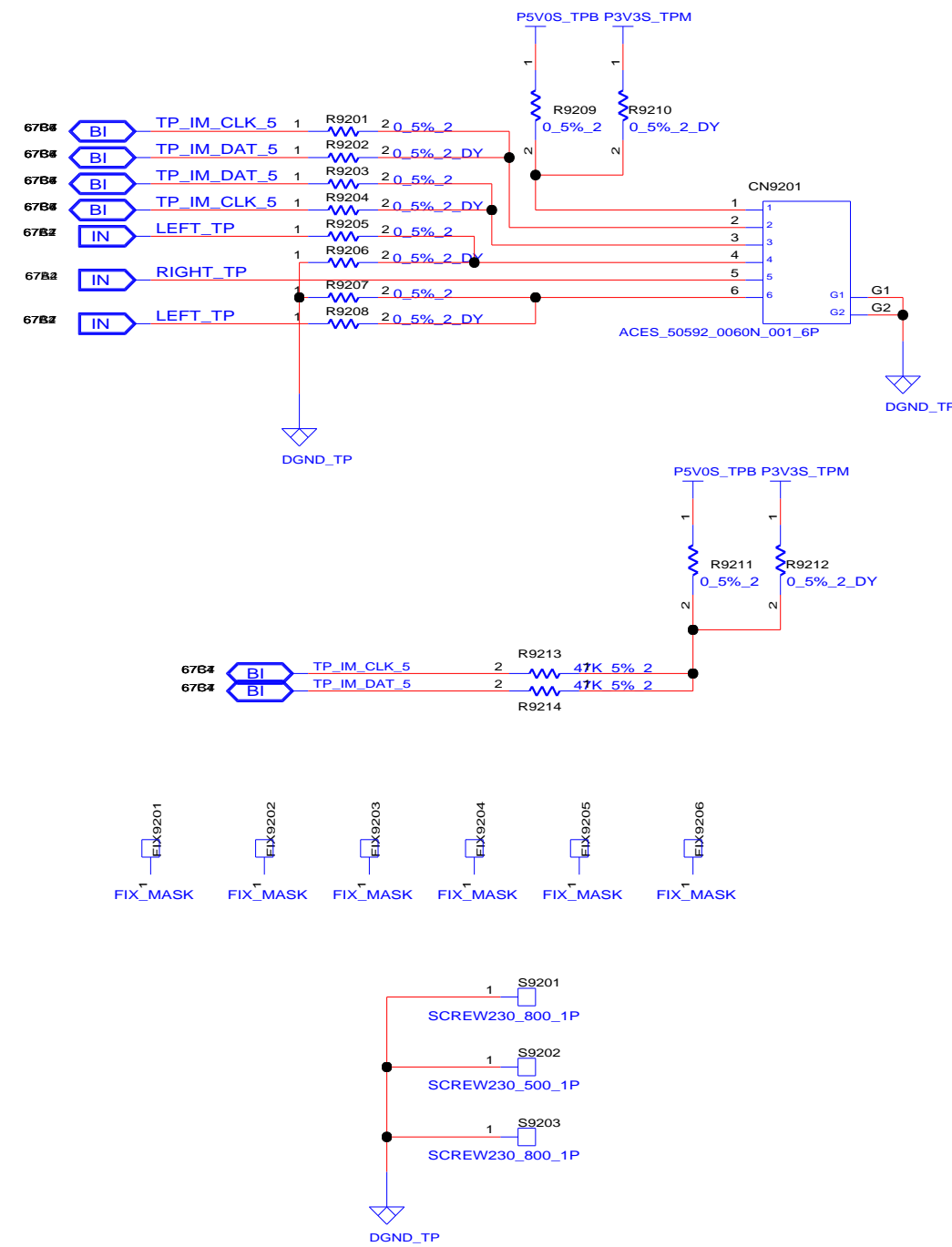
SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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CHANGE by XXX DATE 21-OCT-2002

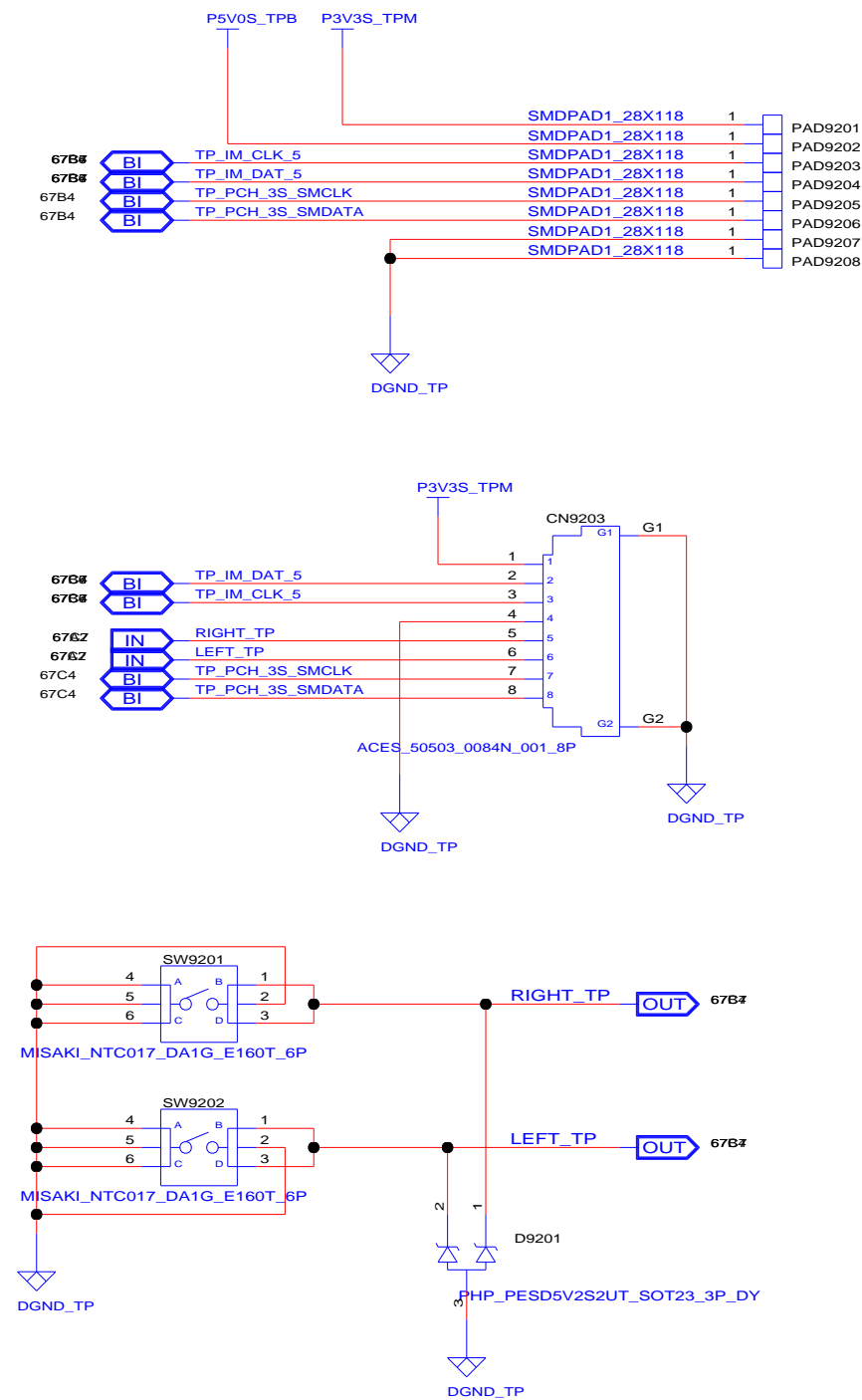
SHEET 66 of 69

# TPUCH PAD BOARD

TOUCHPAD TO MODULE CONN



TOUCHPAD TO MB CONN



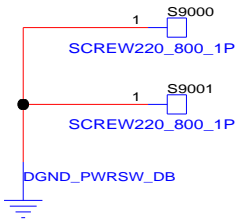
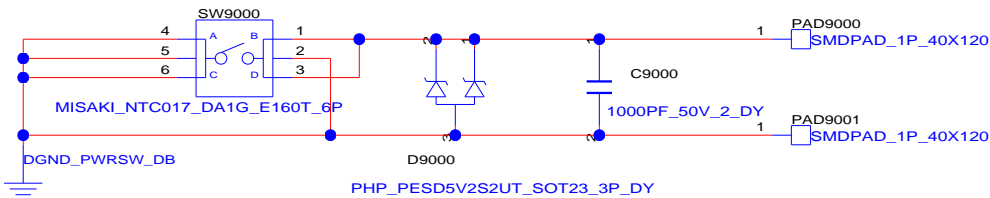
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 67 of 69

REFERENCE 9000~9999(SMALL BOARD)

POWER BUTTON



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
CHANGE by		DATE	SHEET
XXX		21-OCT-2002	68 of 69

